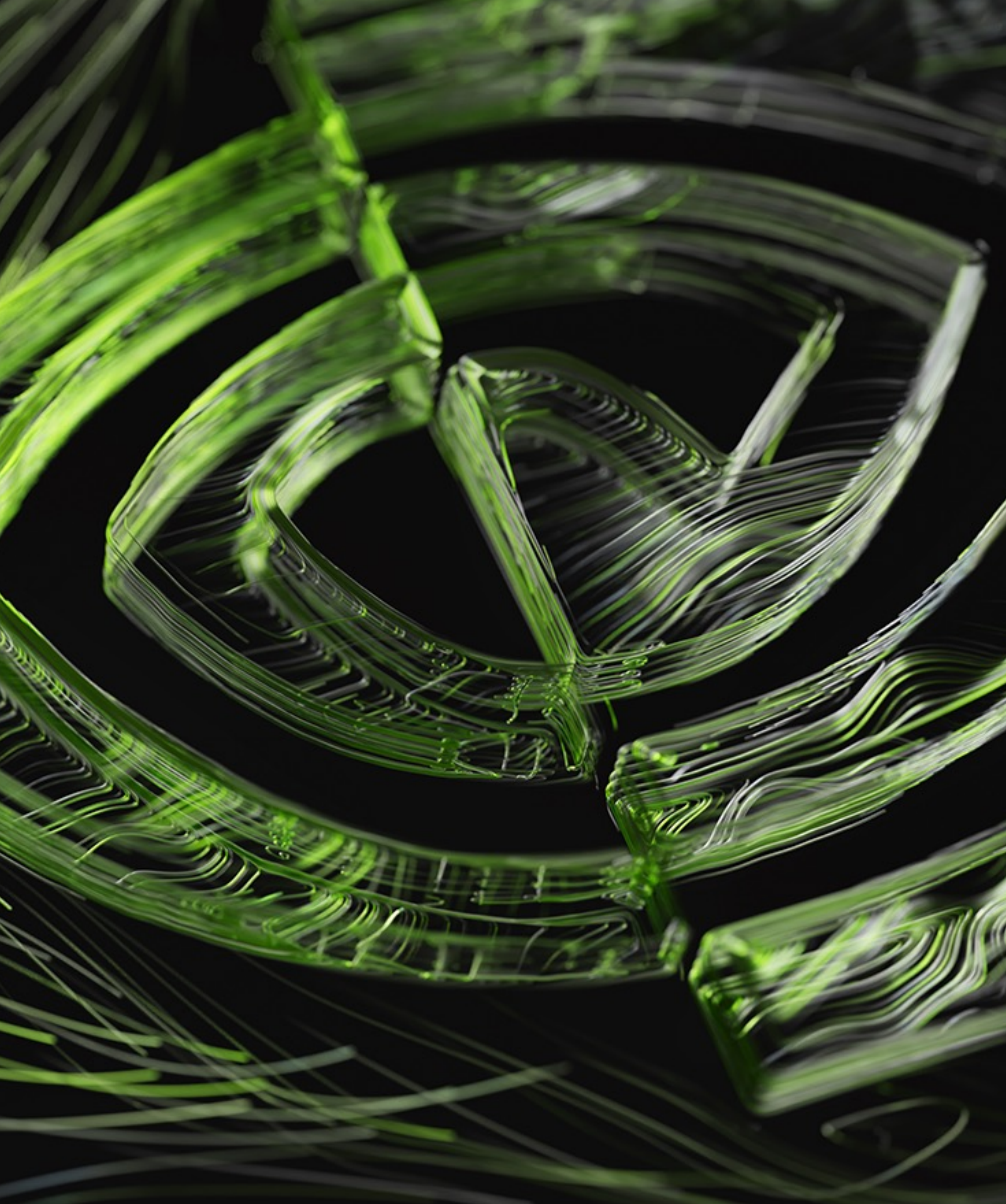


NVIDIA Quantum による 量子回路シミュレータの高速化

Naruhiko Tan, Solution Architect, NVIDIA Japan

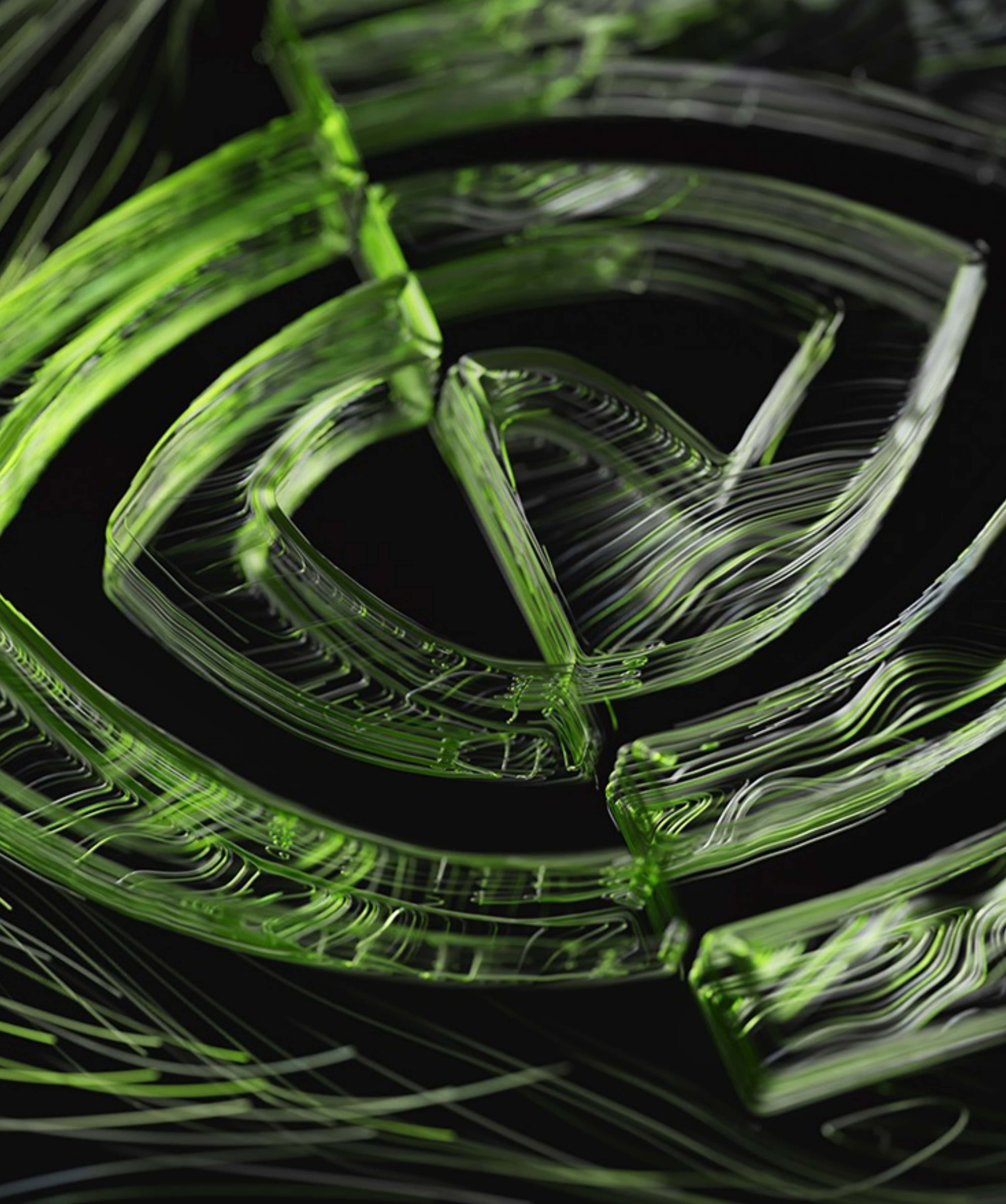


Agenda

- State of Quantum

- Accelerated Computing for Quantum

- Summary



Agenda

- **State of Quantum**

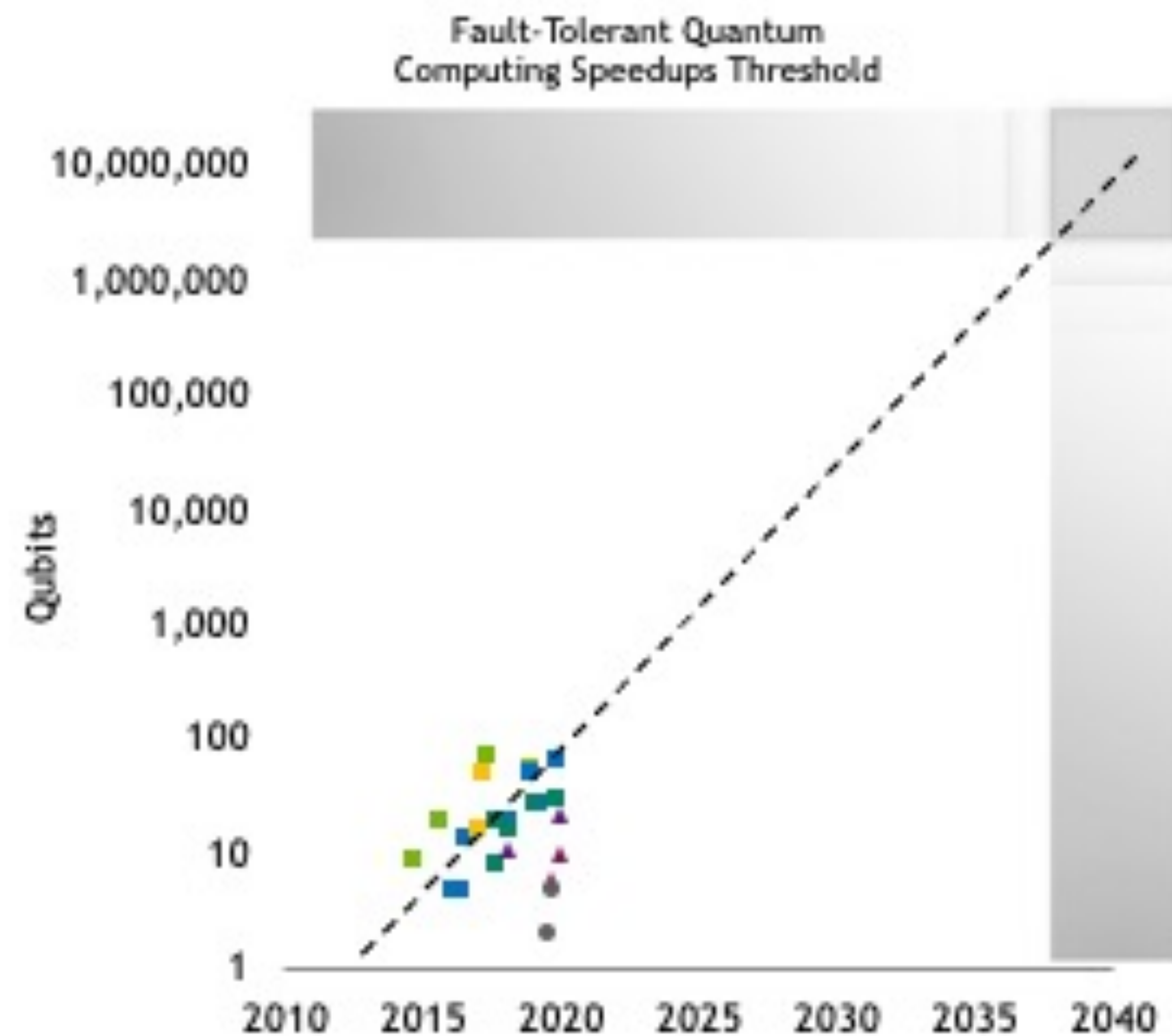
- Accelerated Computing for Quantum

- Summary

Worldwide Effort Towards a New Computing Model

QUANTUM SYSTEMS SCALING EXPONENTIALLY

Useful, Fault-Tolerant Qubit Scale Could Be Achieved in 15 to 20 Years



GOVERNMENT

22+

National Quantum Initiatives

INDUSTRY

70%

Of companies have quantum Initiatives

HIGHER ED/RESEARCH

2,100+

QC Research Papers

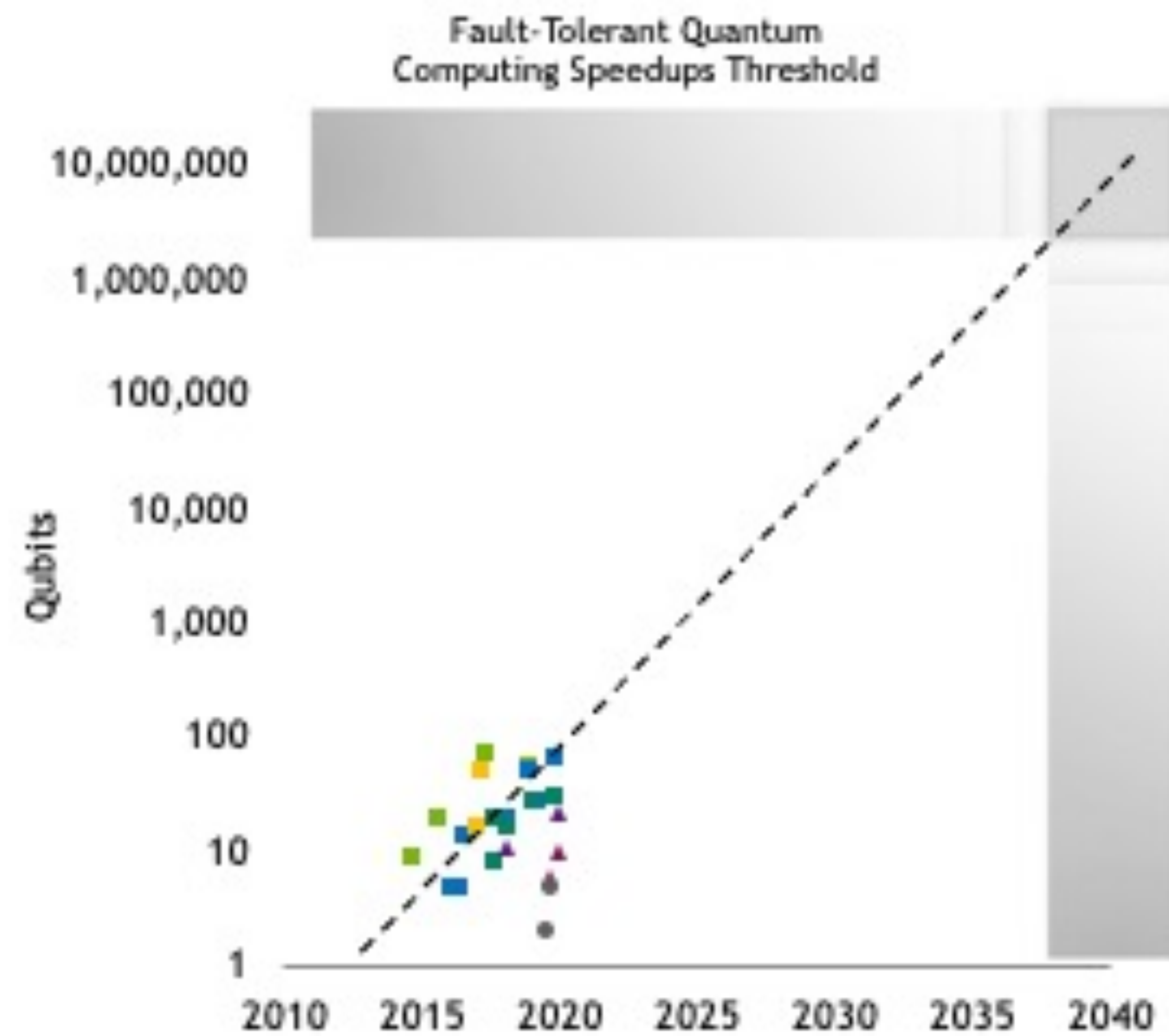
TECHNOLOGY

250+

QC Startups

Challenges for Useful Quantum Computing

QUANTUM SYSTEMS SCALING EXPONENTIALLY
Useful, Fault-Tolerant Qubit Scale Could Be Achieved in 15 to 20 Years



SCALE

100,000+
Total Qubits Required

ALGORITHMS

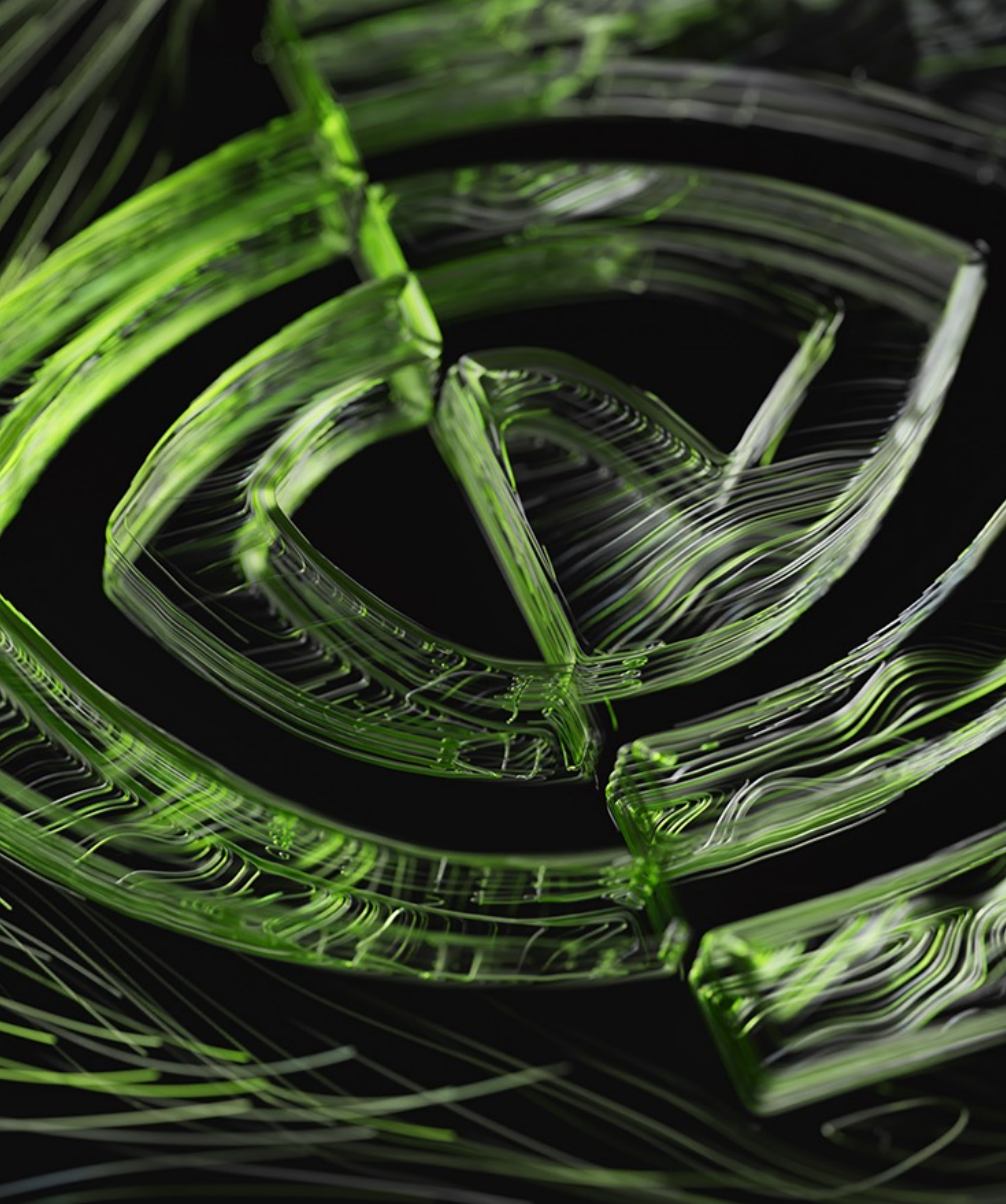
Unknown
Algorithms for Advantage

FIDELITY

1,000+
Perfect Error Corrected Logical Qubits Required

INTEGRATION

Open
Challenge to integrate Quantum in with Classical Supercomputing



Agenda

- State of Quantum

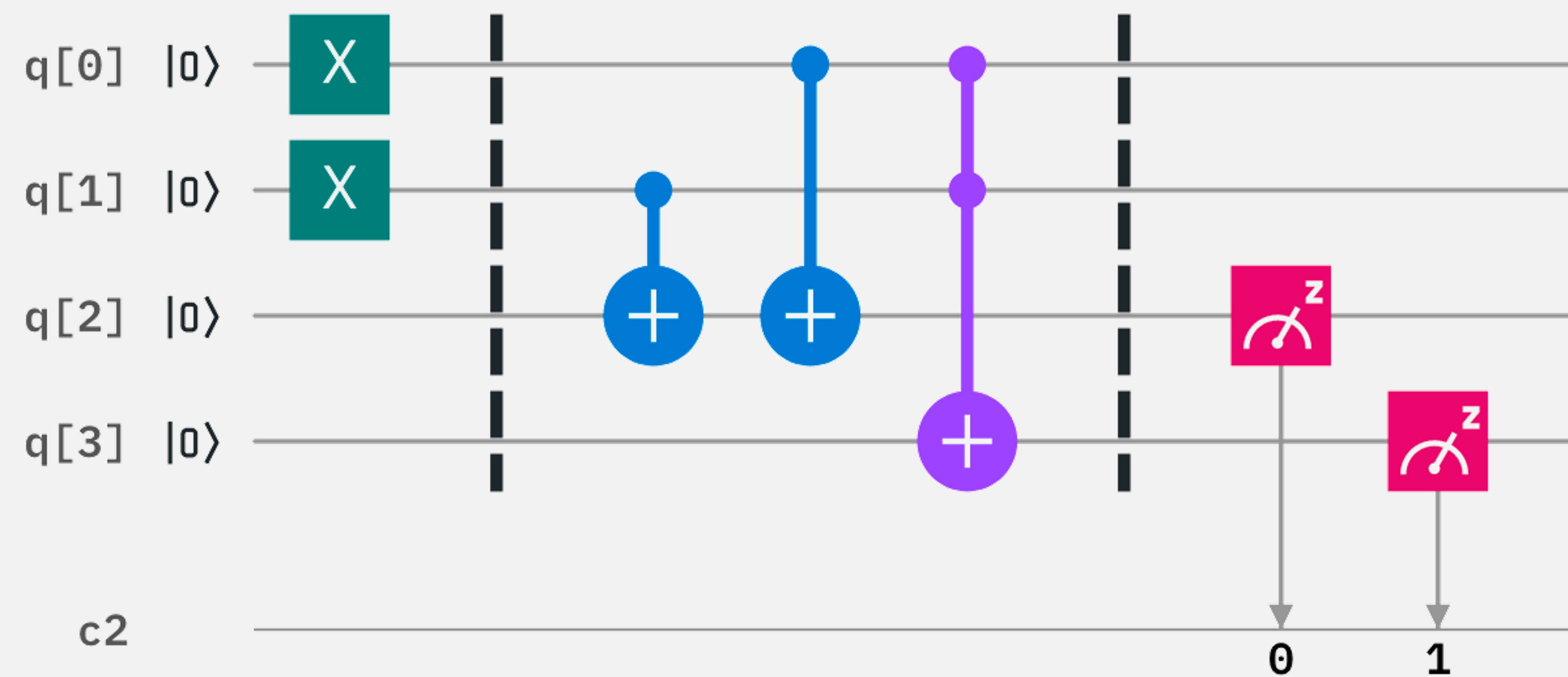
- **Accelerated Computing for Quantum**

- Summary

GPU Supercomputing and Quantum

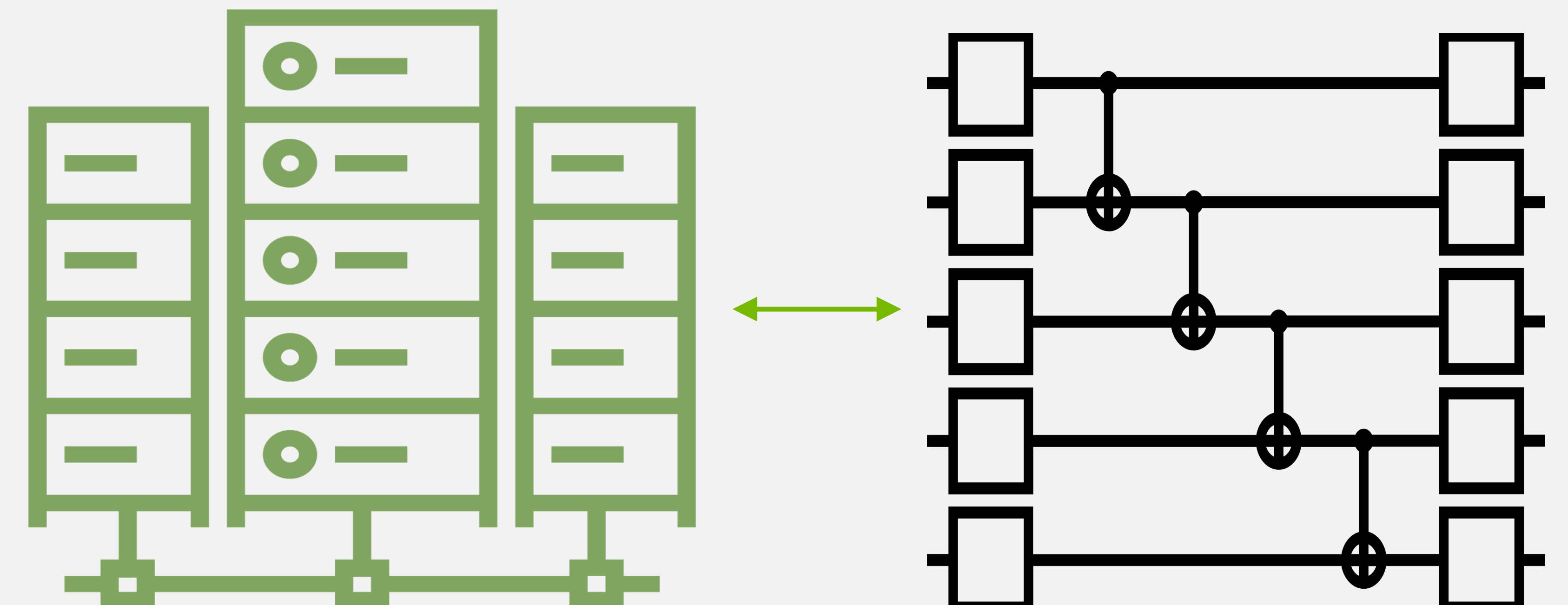
Researching the Quantum Computers of Tomorrow with the Supercomputers of Today

QUANTUM SIMULATION



- Develop algorithms at scale of valuable quantum computing
- Discover use cases with quantum advantage
- Design and validate future hardware

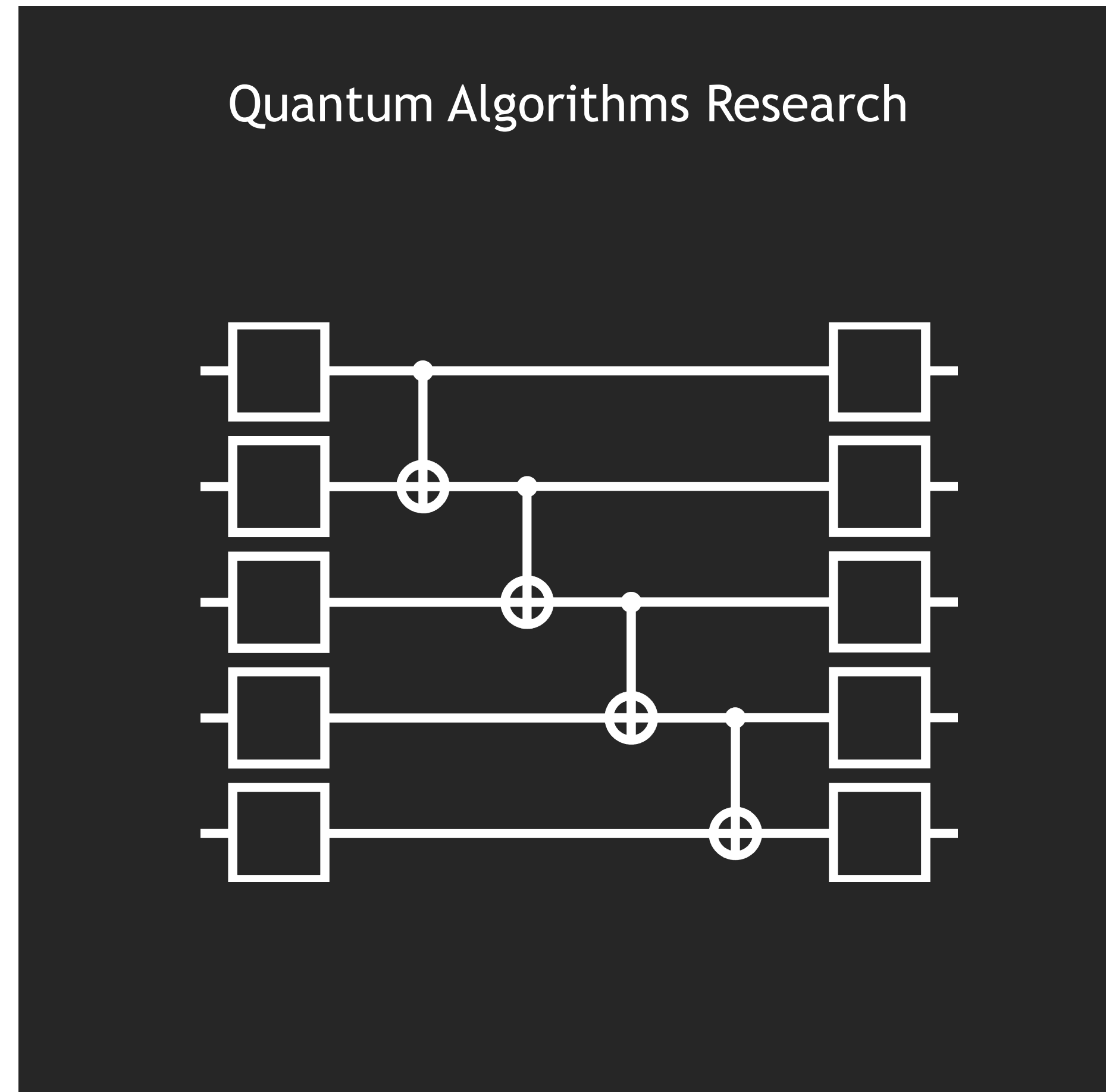
HYBRID QUANTUM-CLASSICAL COMPUTING



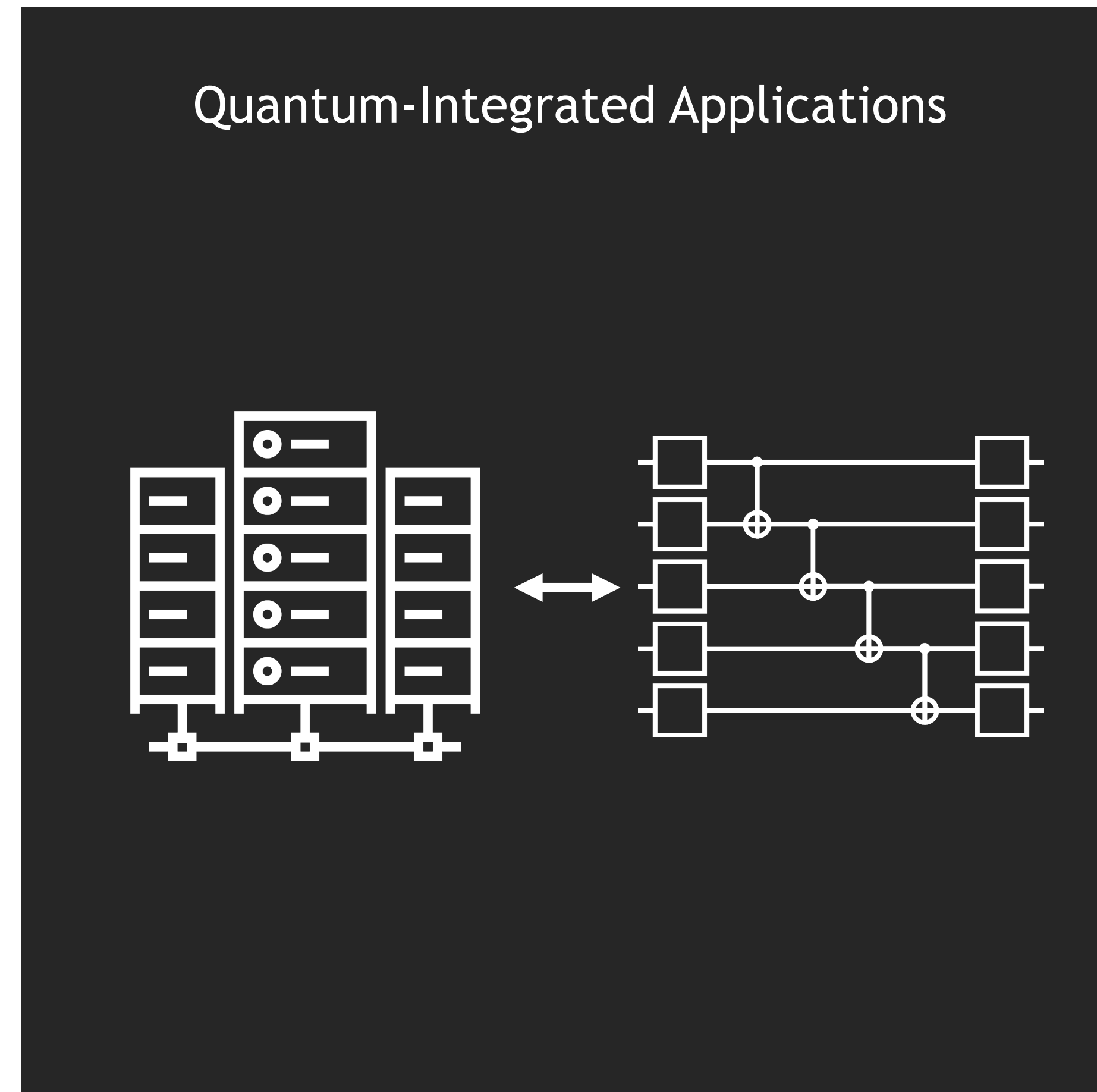
- Integrate quantum into leading accelerated applications
- Unparalleled performance and scientific productivity using the best resource for the task
- GPUs and AI to get the most out of QC

NVIDIA Quantum

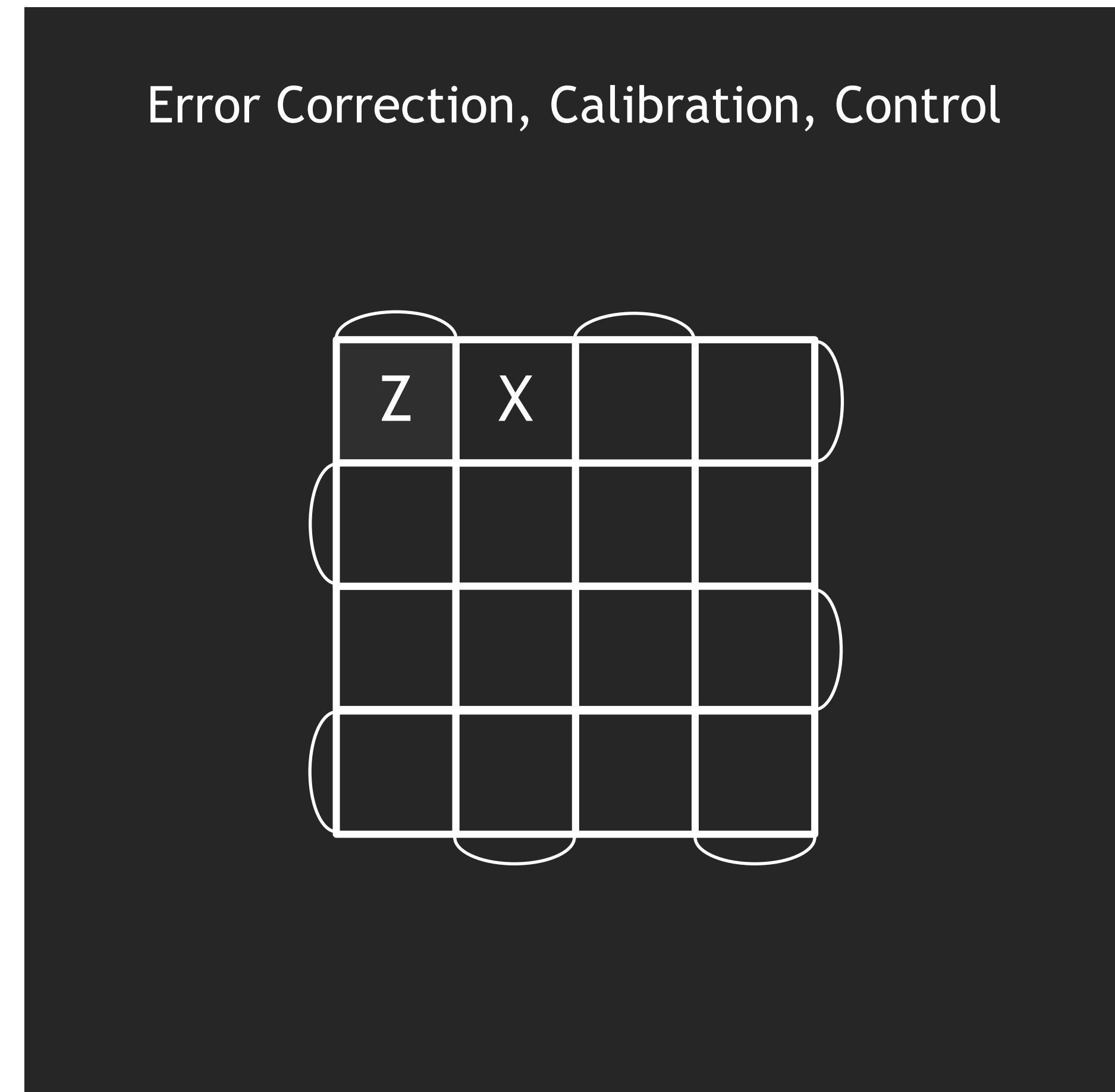
Powering Quantum Simulation and Quantum-Integrated Accelerated Computing



cuQuantum
Accelerated Quantum Simulation



CUDA Quantum
Quantum-Classical Developer Platform

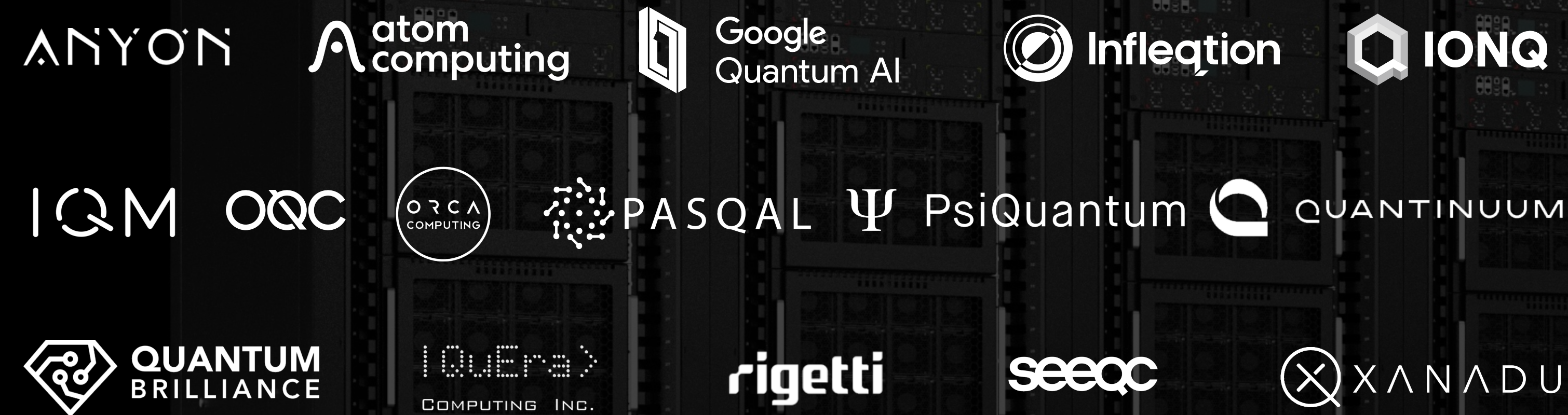


Quantum Integrated GPU Supercomputing
DGX | HGX | DGX Quantum

NVIDIA QUANTUM

Empowering the Quantum Computing Community

QUANTUM HARDWARE BUILDERS



QUANTUM SOFTWARE AND SYSTEMS



QUANTUM SIMULATION FRAMEWORKS



ENTERPRISE PARTNERS



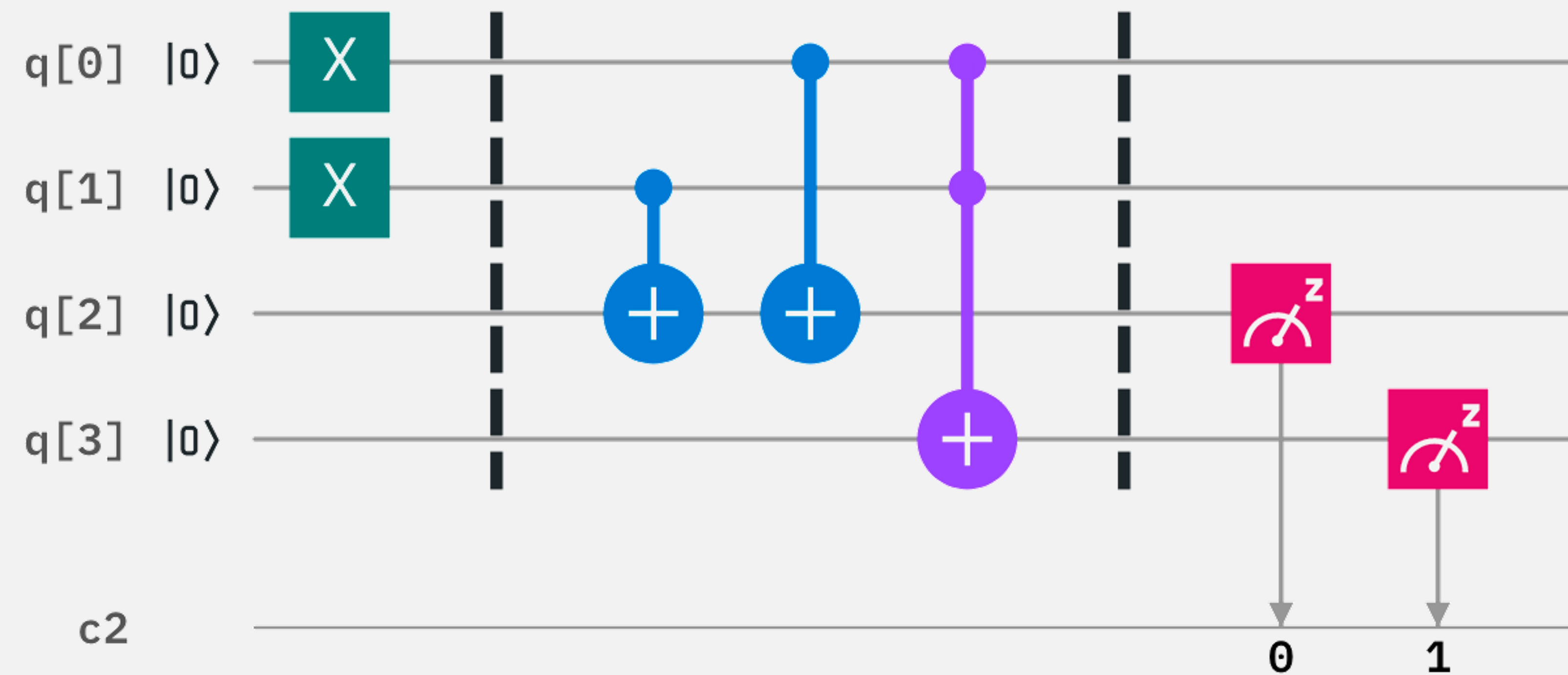
RESEARCH CENTERS



GPU Supercomputing and Quantum

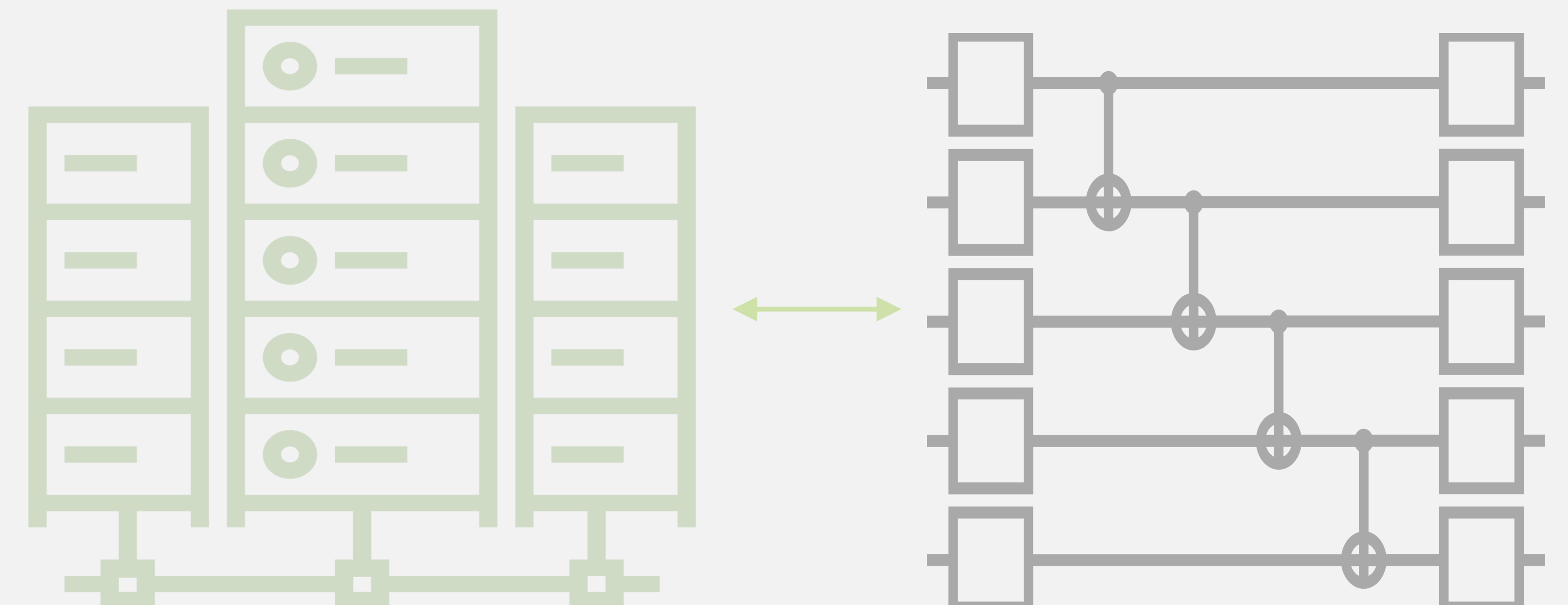
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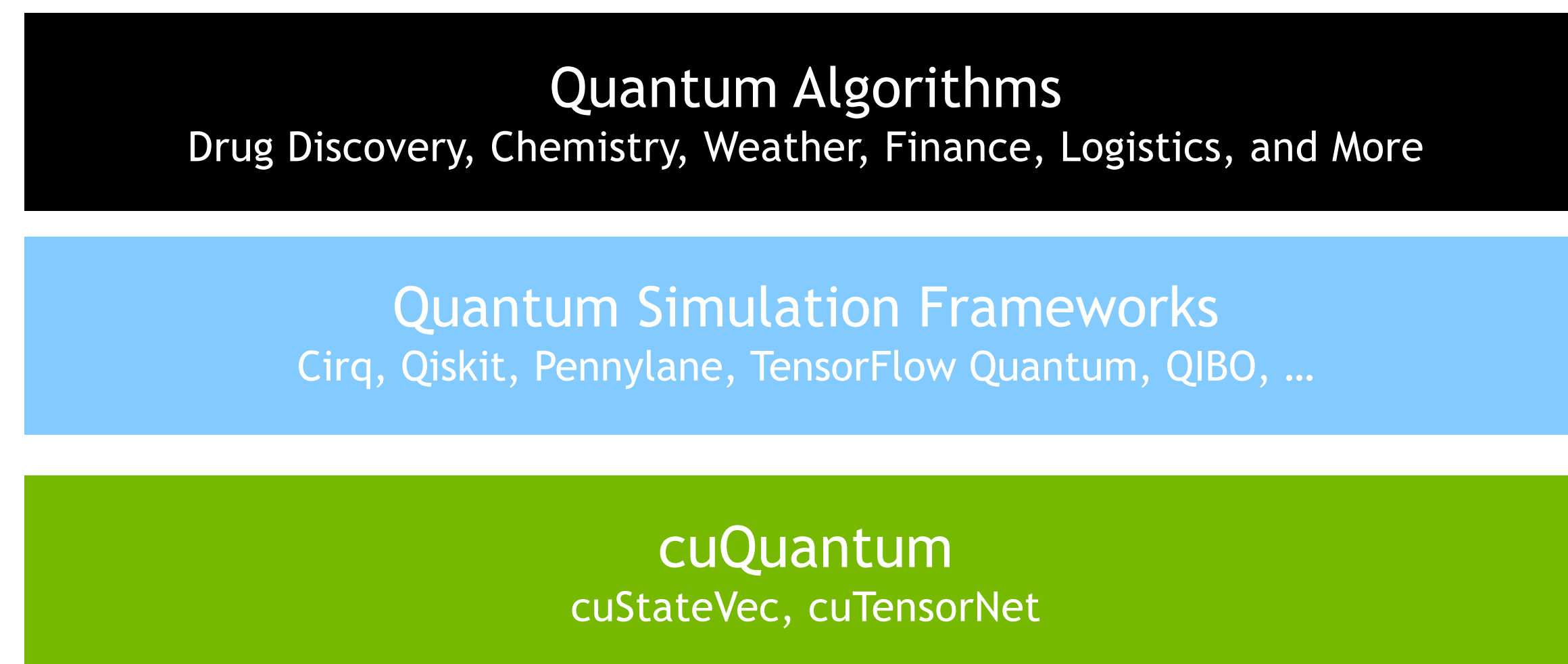


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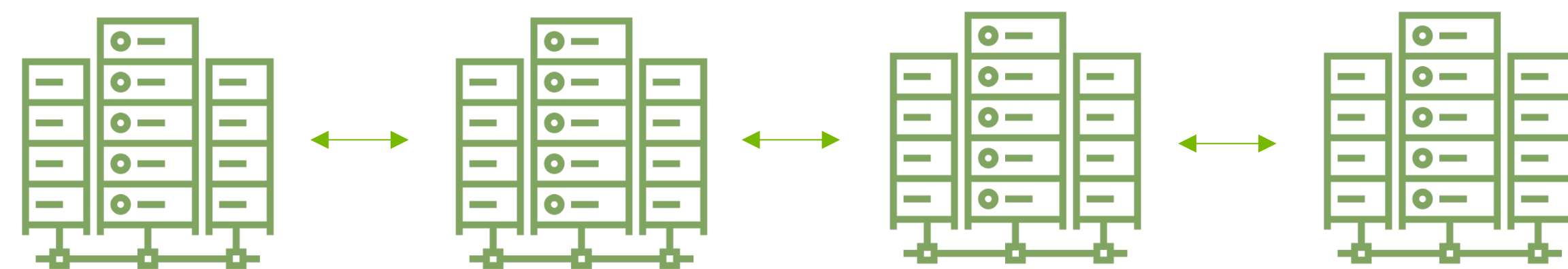
cuQuantum

Research the Quantum Computer of Tomorrow on the most Powerful Computer Today

cuQuantum



GPU Supercomputing



SDK for GPU Accelerated Quantum Simulation

Simulate Ideal or Noisy Qubits with State Vector or Tensor Network methods

Supports GPU Supercomputing with Multi-Node Multi-GPU Circuit Simulation

Integrated into all leading frameworks

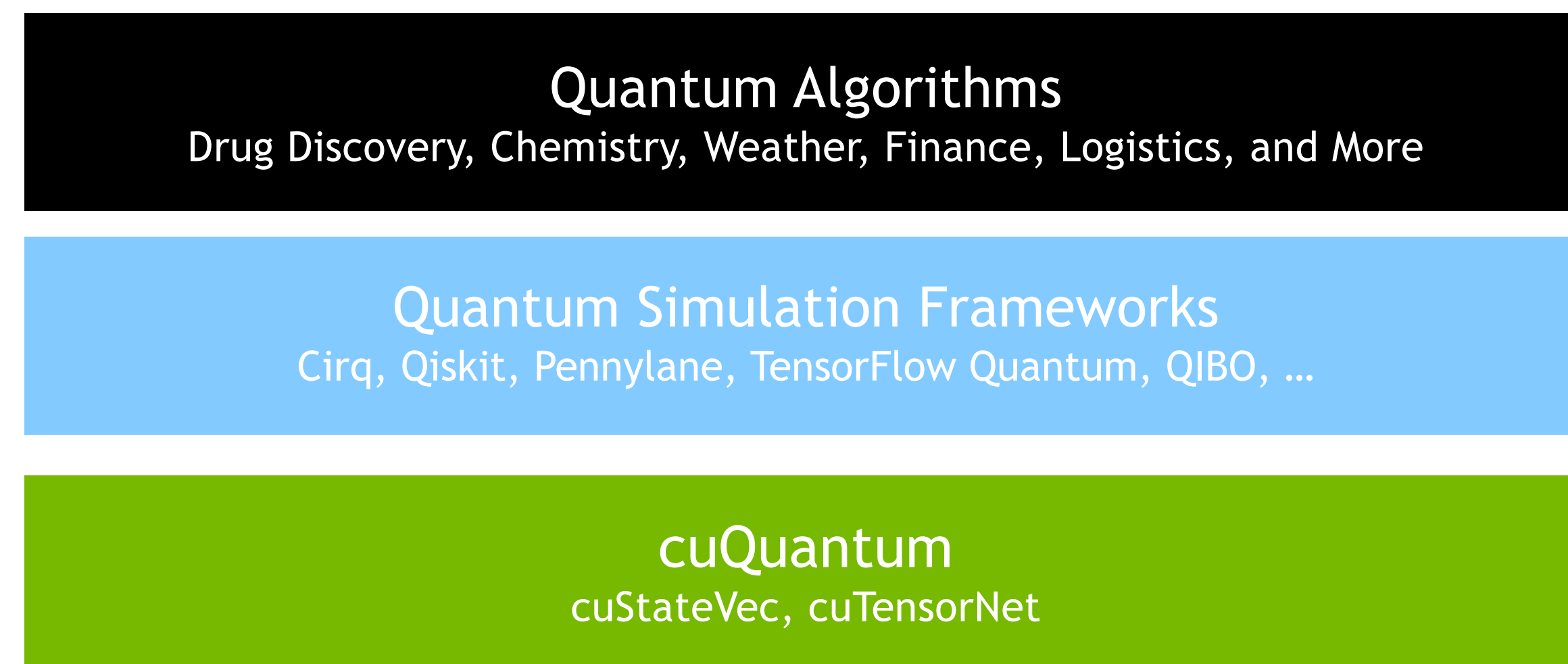
Optimized frameworks in cuQuantum Appliance:
catalog.ngc.nvidia.com/orgs/nvidia/containers/cuquantum-appliance

- **Now available as VMI on all major Clouds**

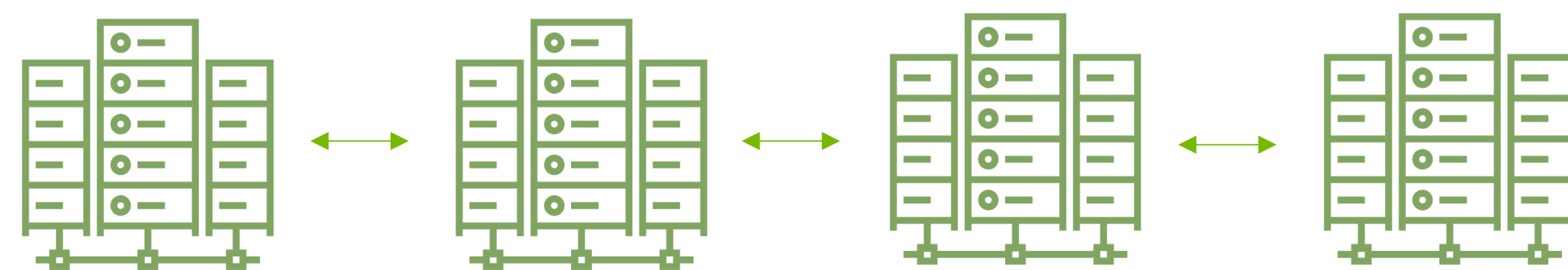
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GPU Supercomputing



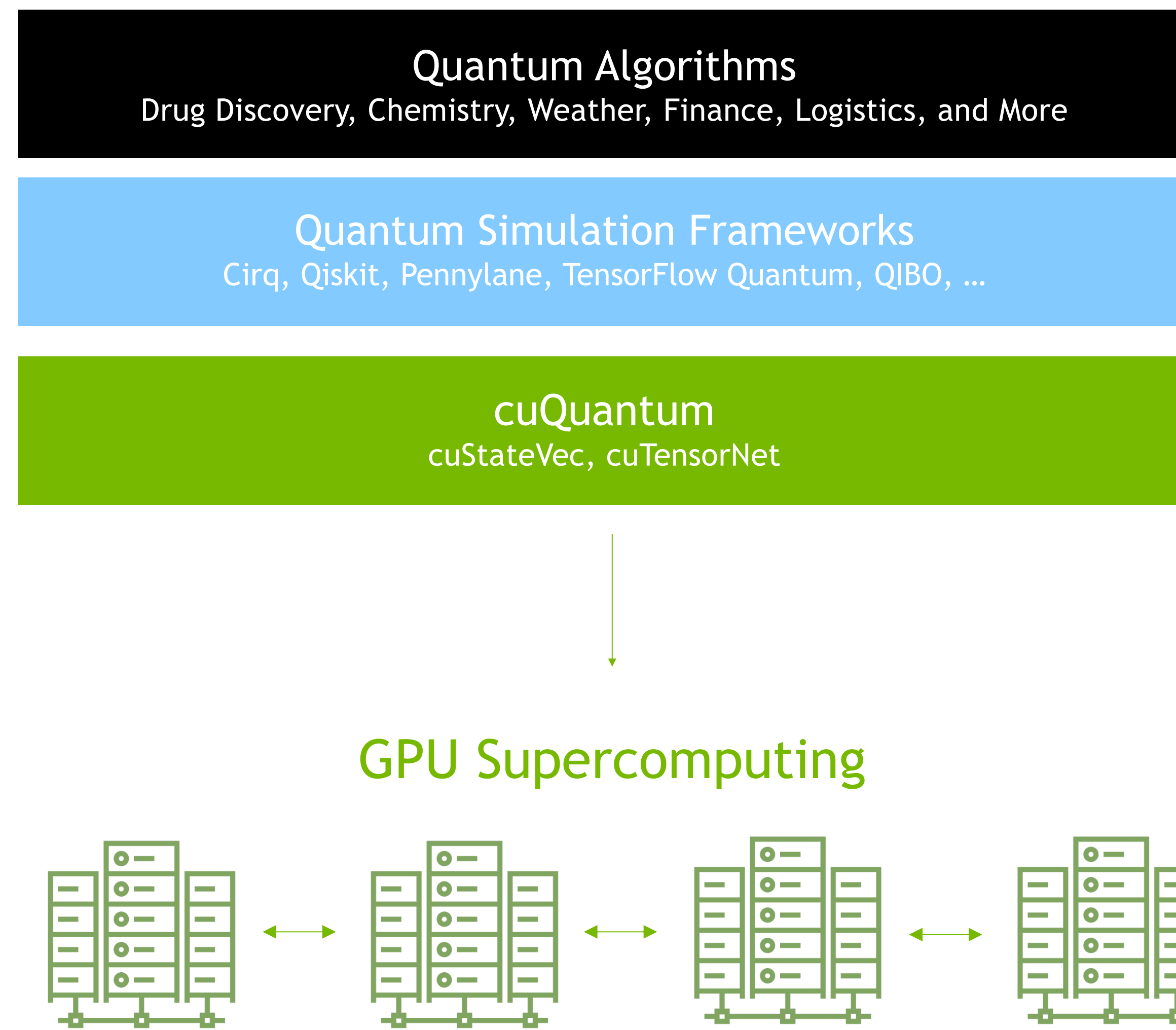
cuQuantum Accelerated Frameworks



cuQuantum

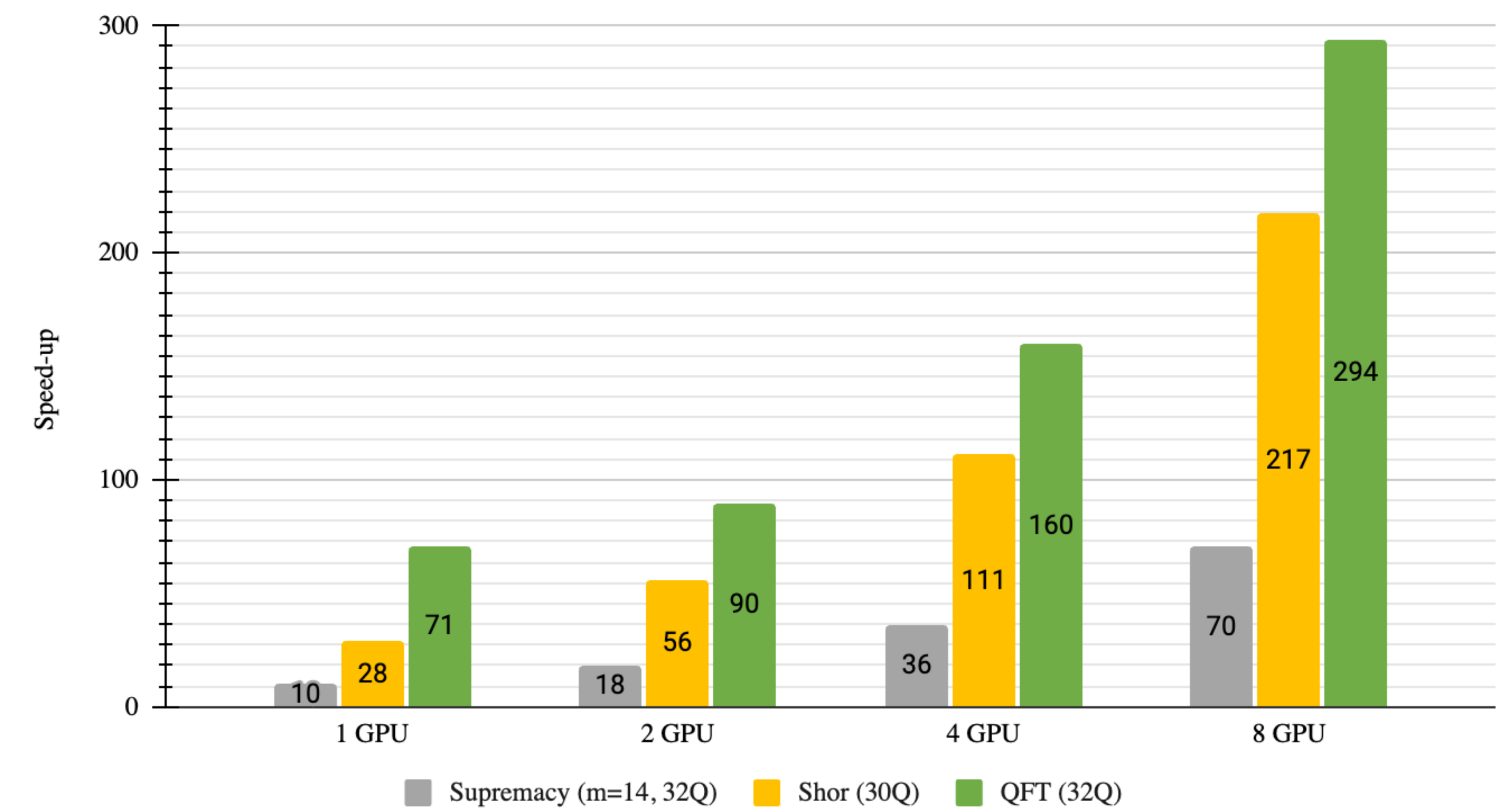
Research the Quantum Computer of Tomorrow on the most Powerful Computer Today

cuQuantum



World Class Performance

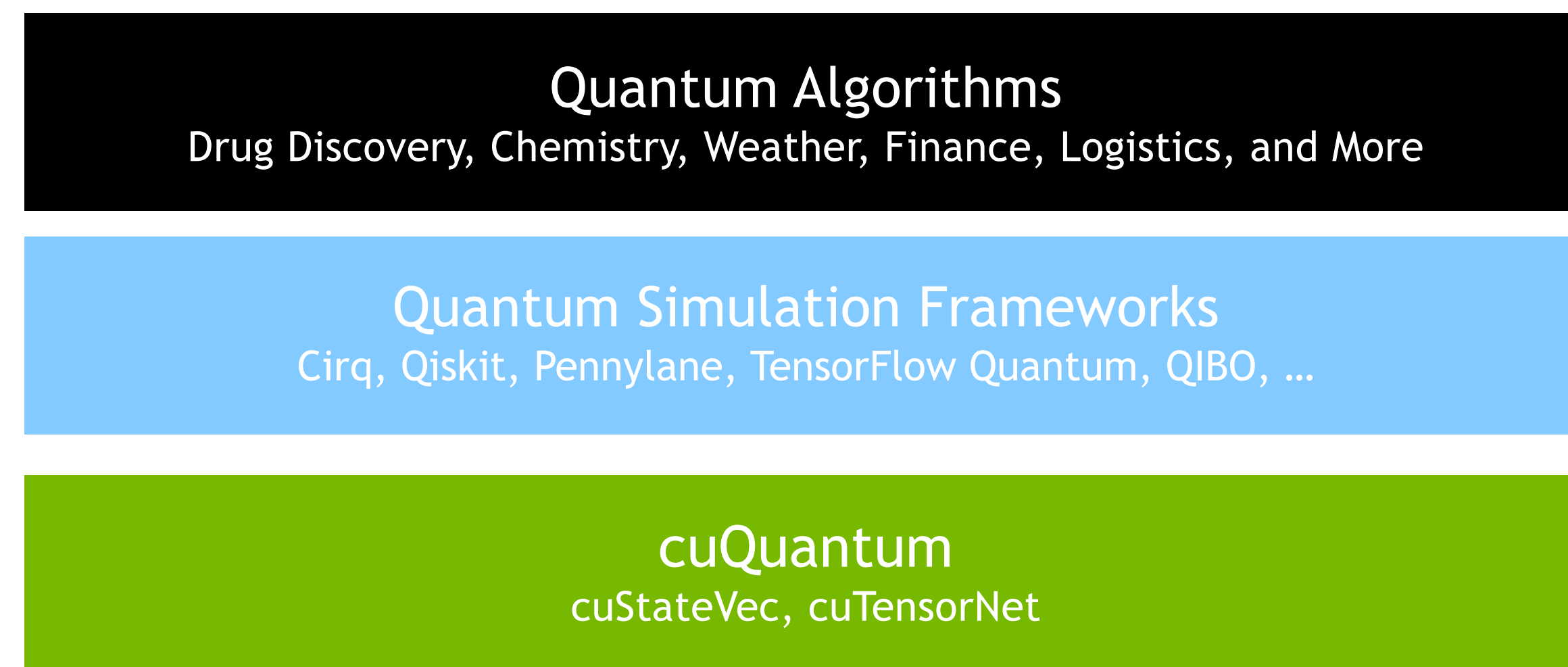
Multi-GPU Speedup of Cirq with cuQuantum on DGX A100



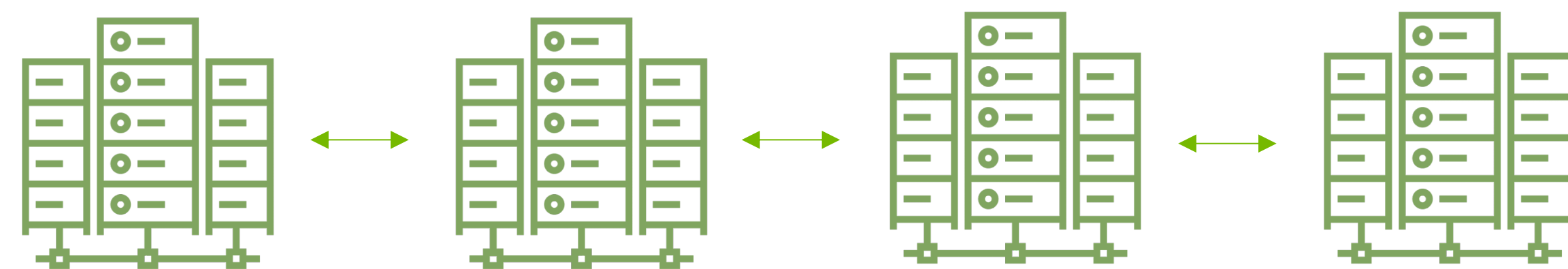
cuQuantum

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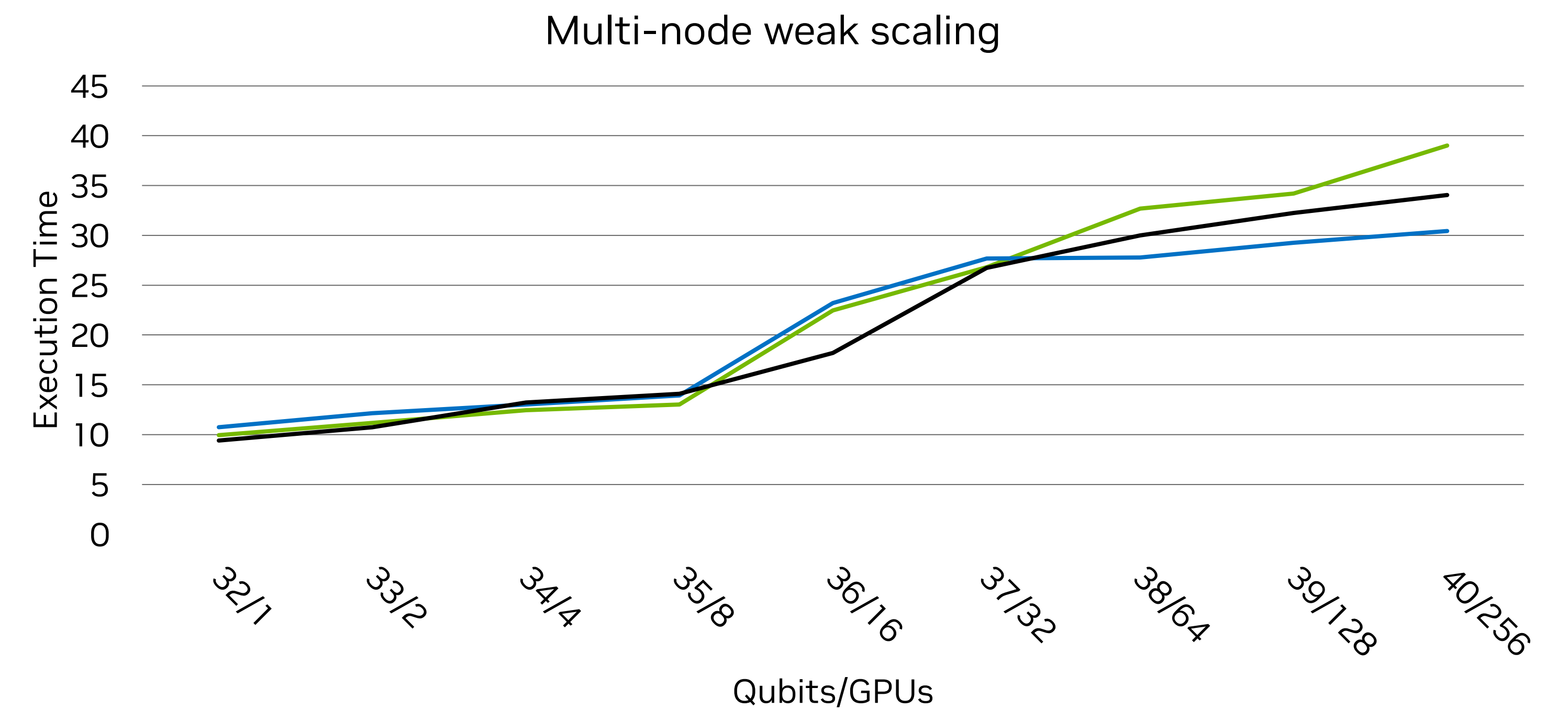
cuQuantum



GPU Supercomputing



World Class Performance with Now with Multi-Node Multi-GPU



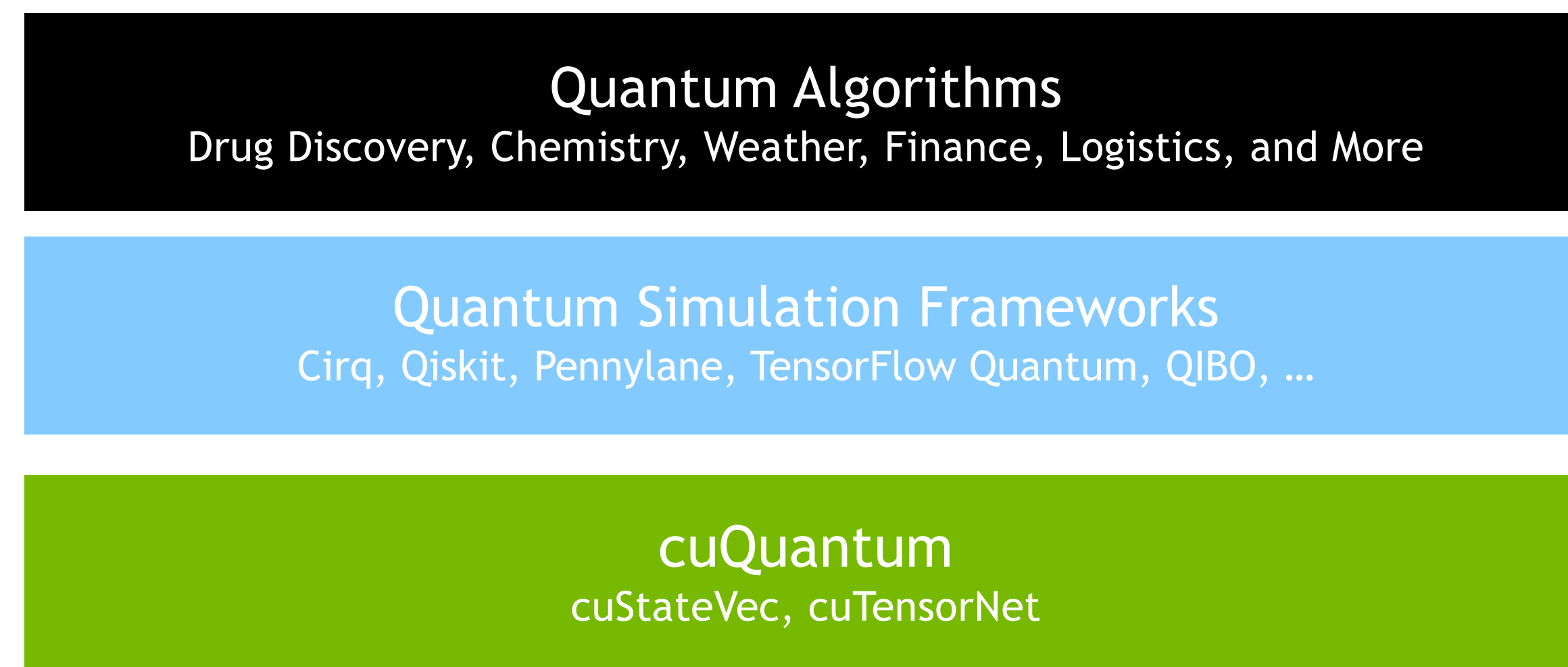
80x speedup over SotA

- Quantum Volume, depth=30
- QAOA
- Quantum Phase Estimation

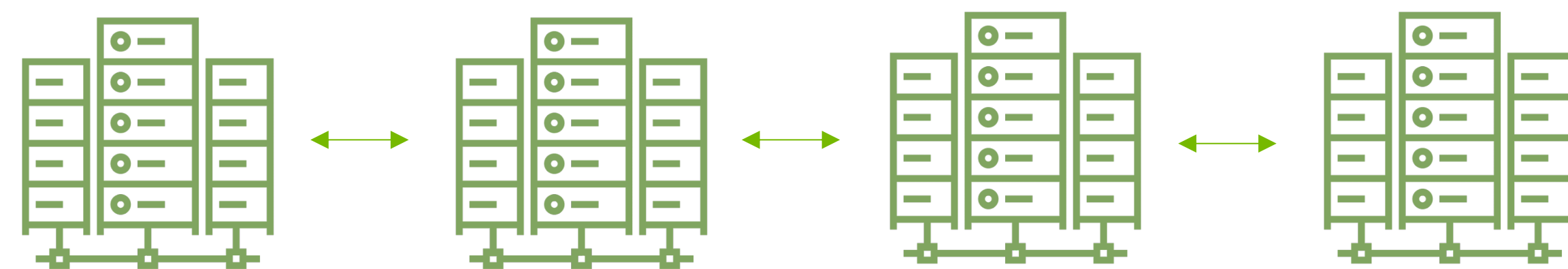
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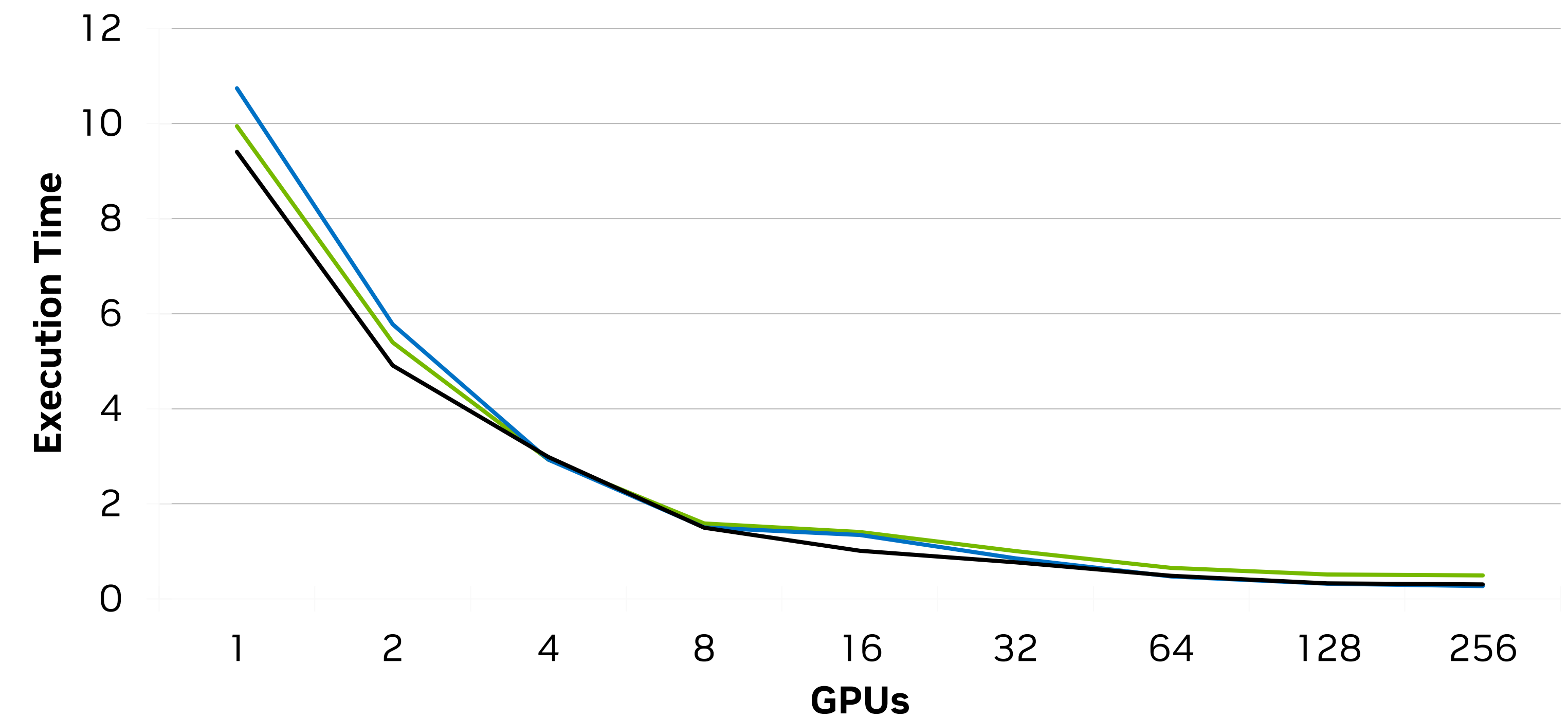


GPU Supercomputing



World Class Performance with Now with Multi-Node Multi-GPU

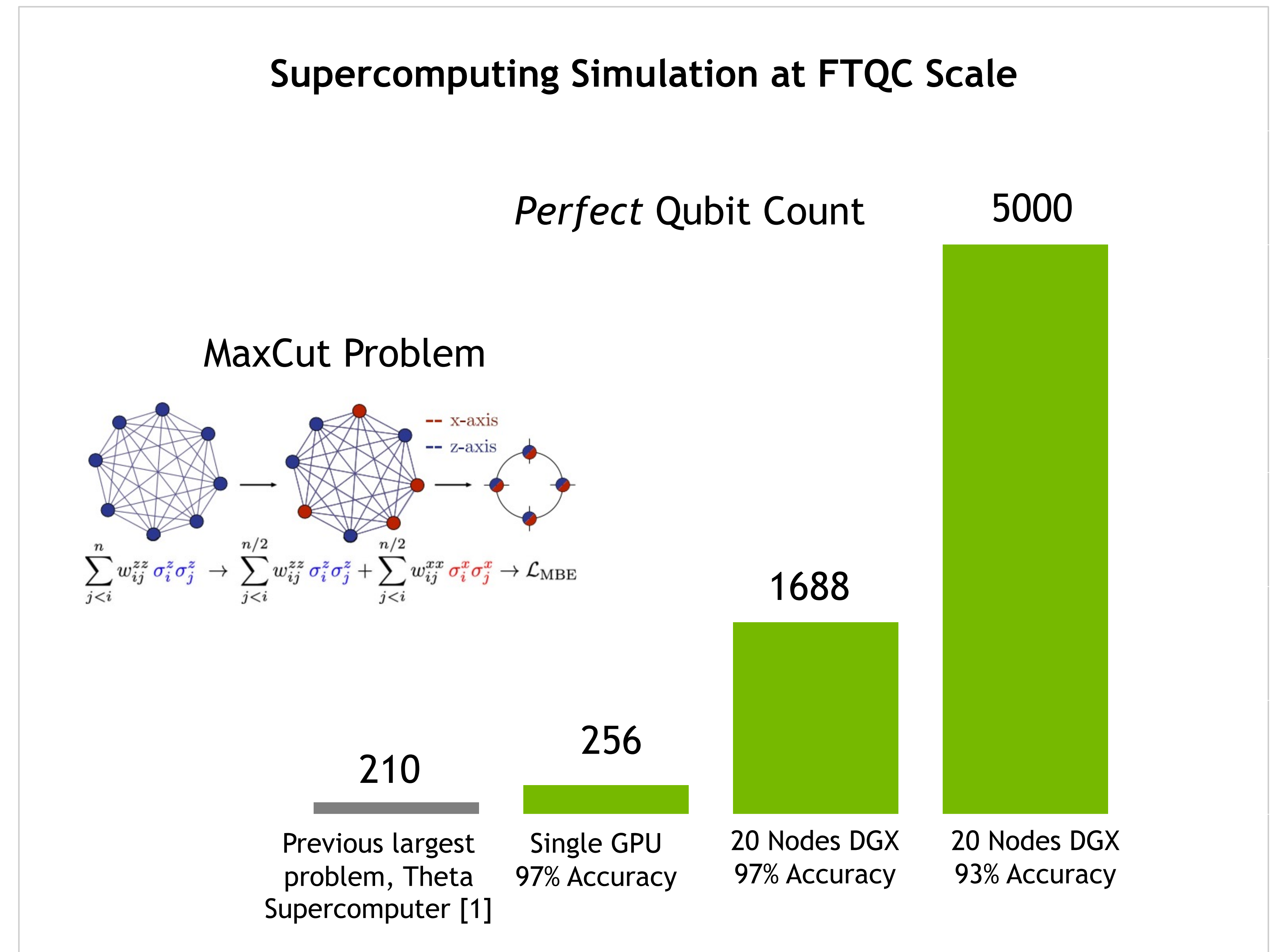
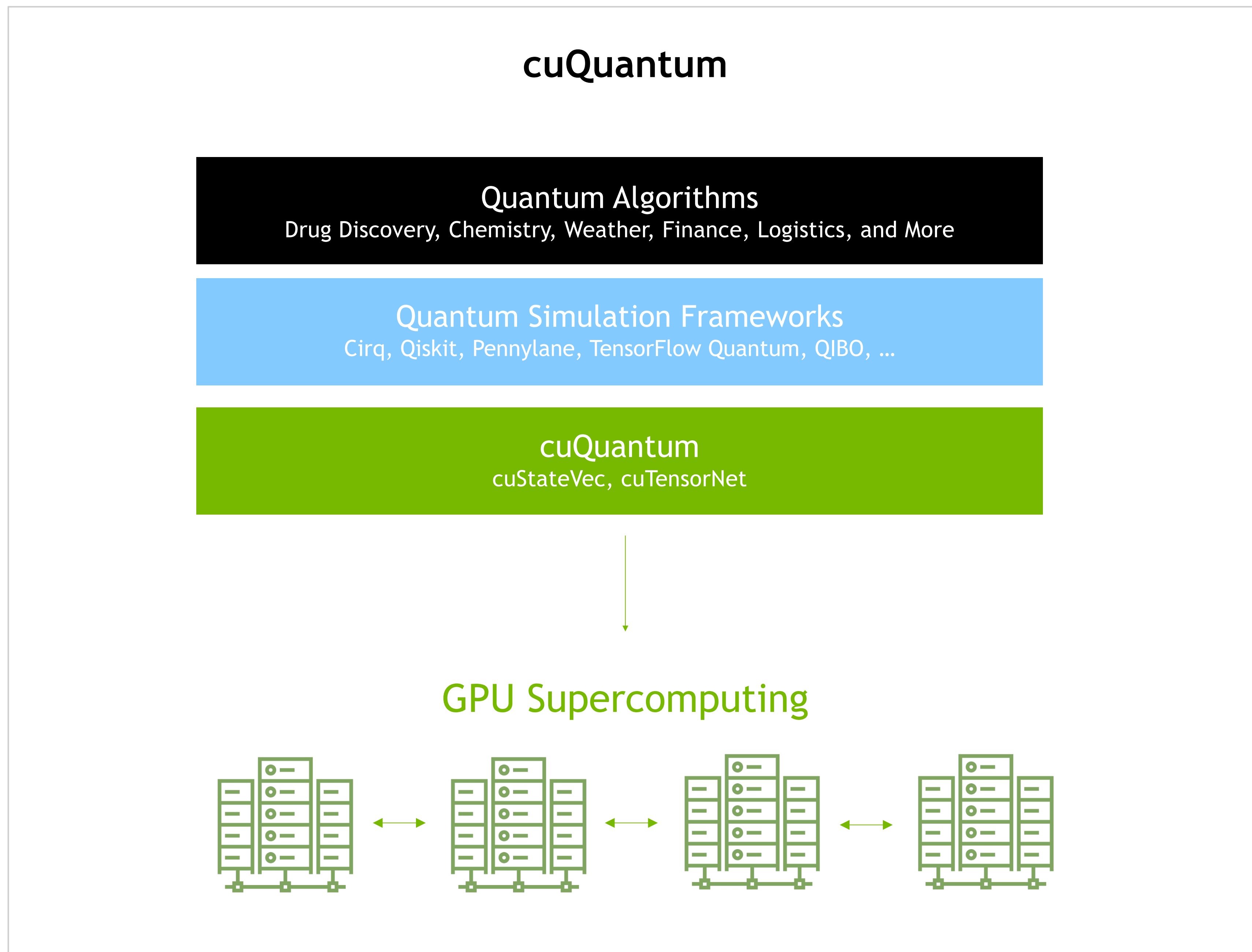
Multi-node strong scaling for 32 qubits



- Quantum Volume, depth=30
- QAOA
- Quantum Phase Estimation

cuQuantum

Research the Quantum Computer of Tomorrow on the most Powerful Computer Today

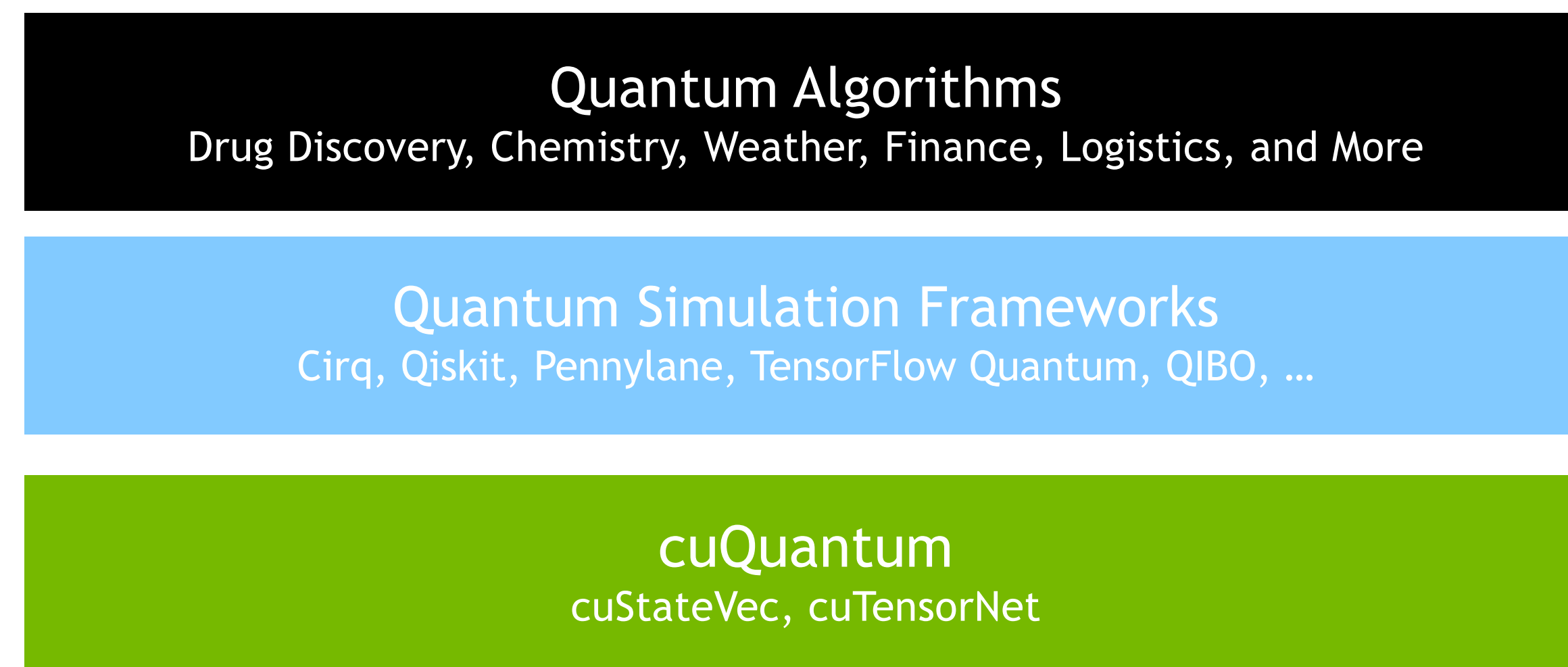


[1] Danylo Lykov et al, Tensor Network Quantum Simulator With Step-Dependent Parallelization, 2020 <https://arxiv.org/pdf/2012.02430.pdf>

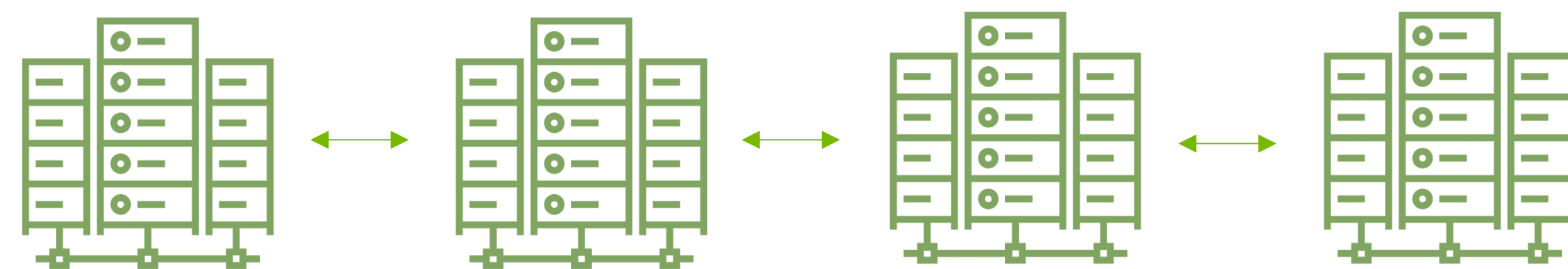
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GPU Supercomputing



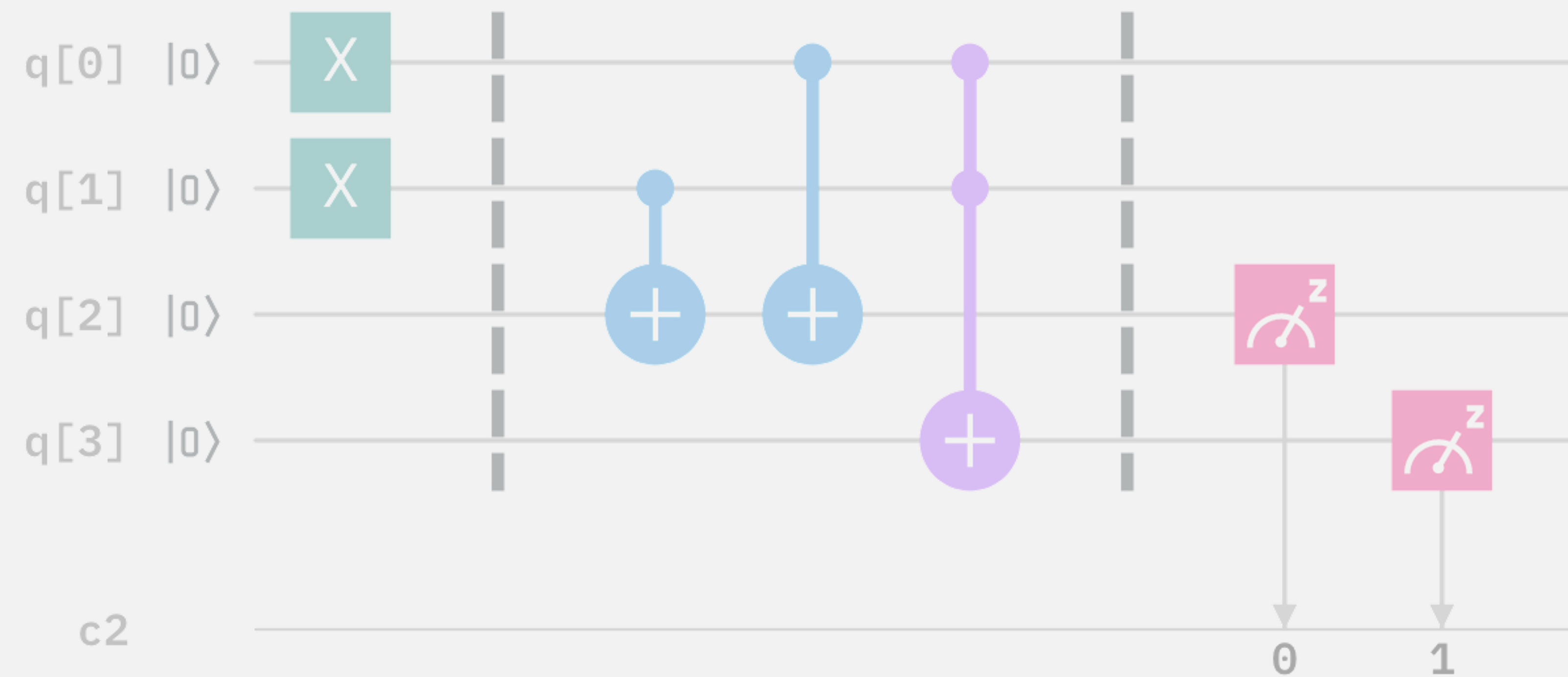
Powering Industrial Research



GPU Supercomputing and Quantum

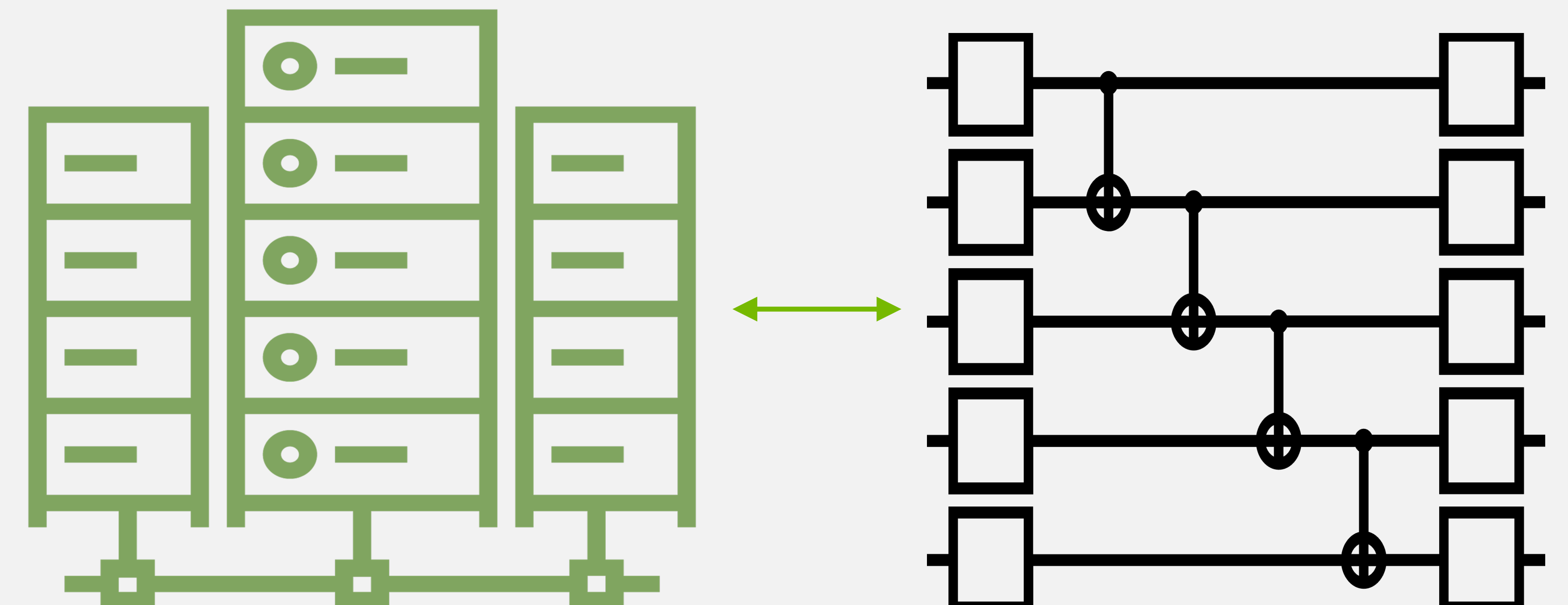
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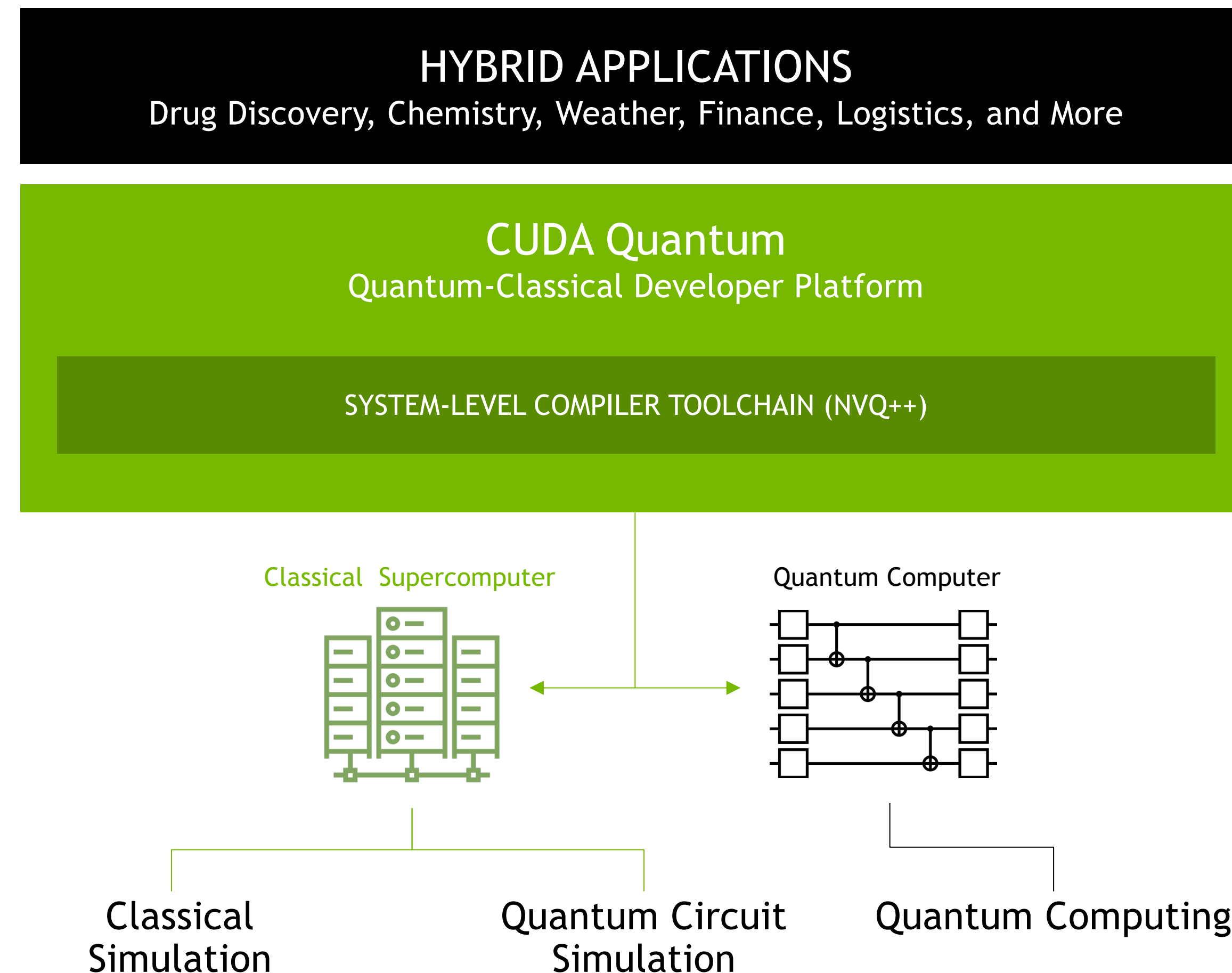


- Integrate quantum into leading accelerated applications
- Unparalleled performance and scientific productivity using the best resource for the task
- GPUs critical for QEC, calibration, hybrid algorithms

CUDA Quantum: Now Available on GitHub and NGC

A Platform For Quantum-Classical Computing

CUDA QUANTUM PLATFORM



CUDA QUANTUM FEATURES

Supports any kind of QPU, emulated or physical

Compiler for hybrid systems

Open and interoperable with today's applications

Single source C++ and Python programming model

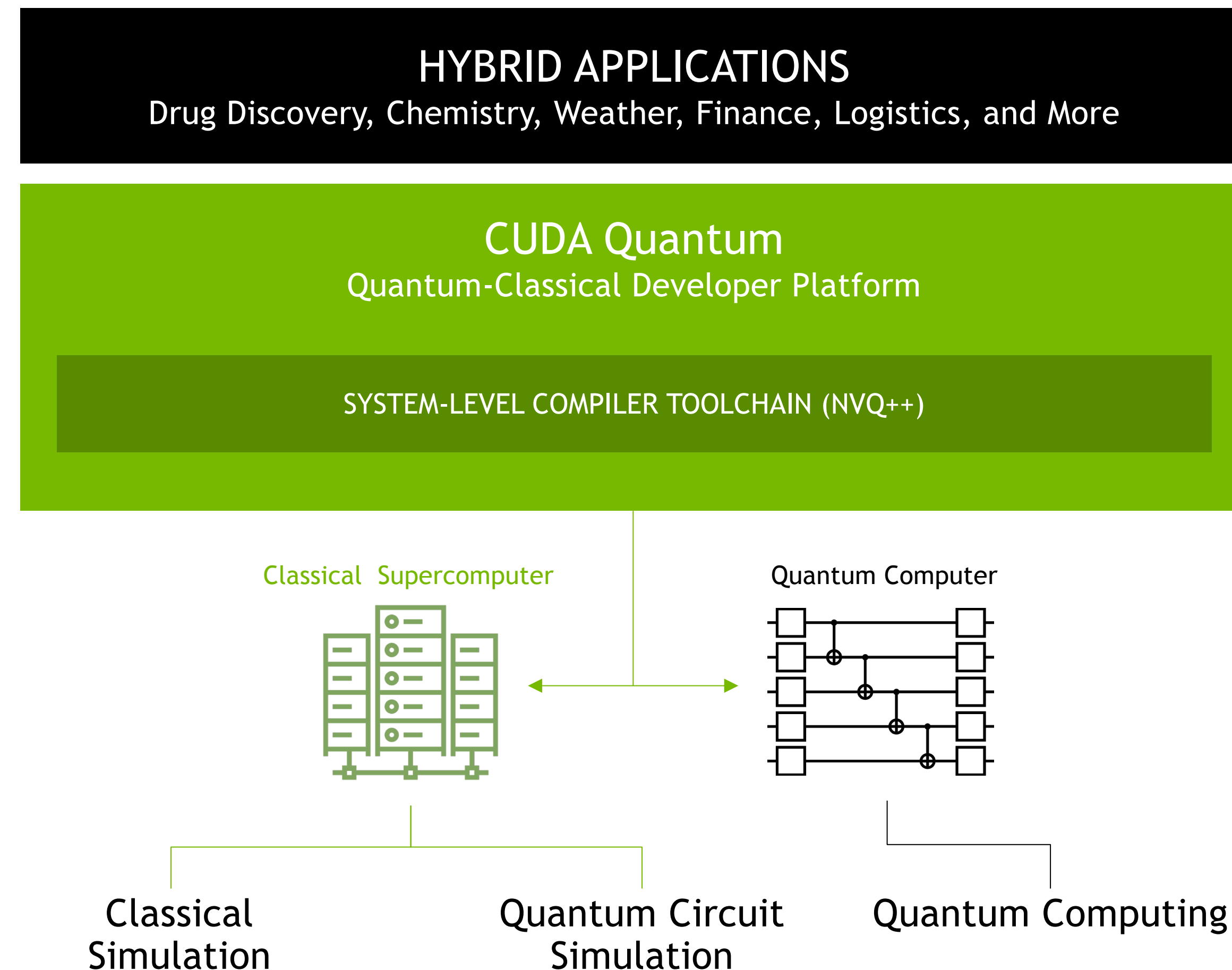
github.com/nvidia/cuda-quantum

<https://catalog.ngc.nvidia.com/orgs/nvidia/containers/cuda-quantum>

CUDA Quantum: Now Available on GitHub and NGC

Natively Hybrid And Interoperable With GPU Supercomputing

CUDA QUANTUM PLATFORM



Interoperable with GPU Supercomputing



```
auto cnts = cudaq::sample(q, ...);
```

```
std::sort(std::execution::par, ...);
```

CUDA

```
kernel<<<...>>>(...);  
cudaDeviceSynchronize();
```

OpenMP

```
#pragma omp target teams loop  
for (...) ...
```

OpenACC

```
#pragma acc parallel loop  
for (...) ...
```

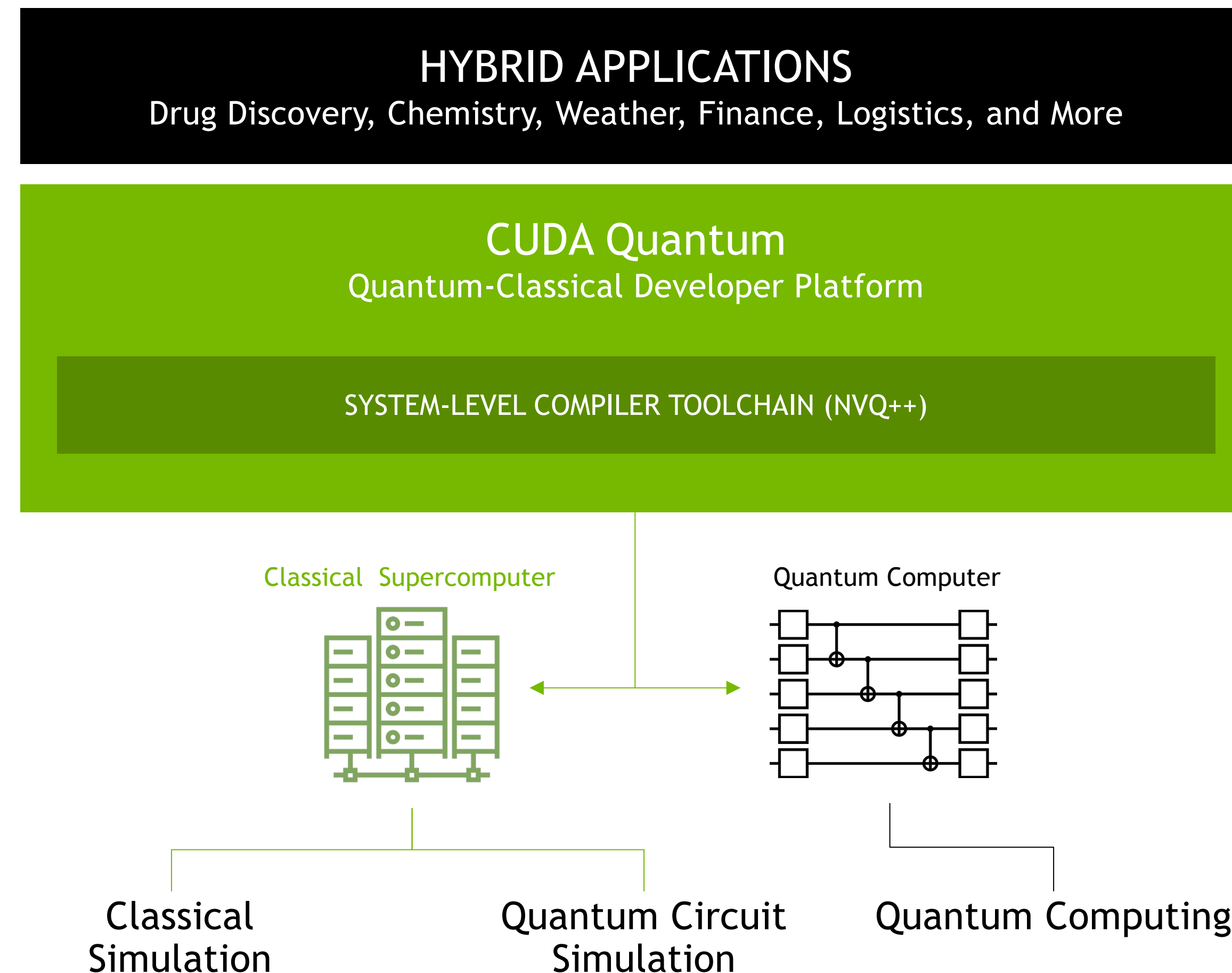
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CUDA QUANTUM PLATFORM



Interoperable with GPU Supercomputing

```
// Compute expectation values with QPU.  
cudaq::spin_op h = ...;  
std::vector<double> sig_exps;  
for (auto& pauli_op : generate_pauli_permutations(h.n_qubits()))  
    sig_exps.push_back(cudaq::observe(qite, pauli_op, ...));
```

```
...  
// Compute LU Factorization of S_mat on the GPU.  
auto dim = std::pow(2, h.n_qubits());  
cusolverDnXgetrf(handle, params, dim, dim, CUDA_C_64F, S_mat,  
                  lda, NULL, CUDA_C_64F,  
                  buffer_on_device,  
                  bytes_on_device, buffer_on_host,  
                  bytes_on_host, info);
```

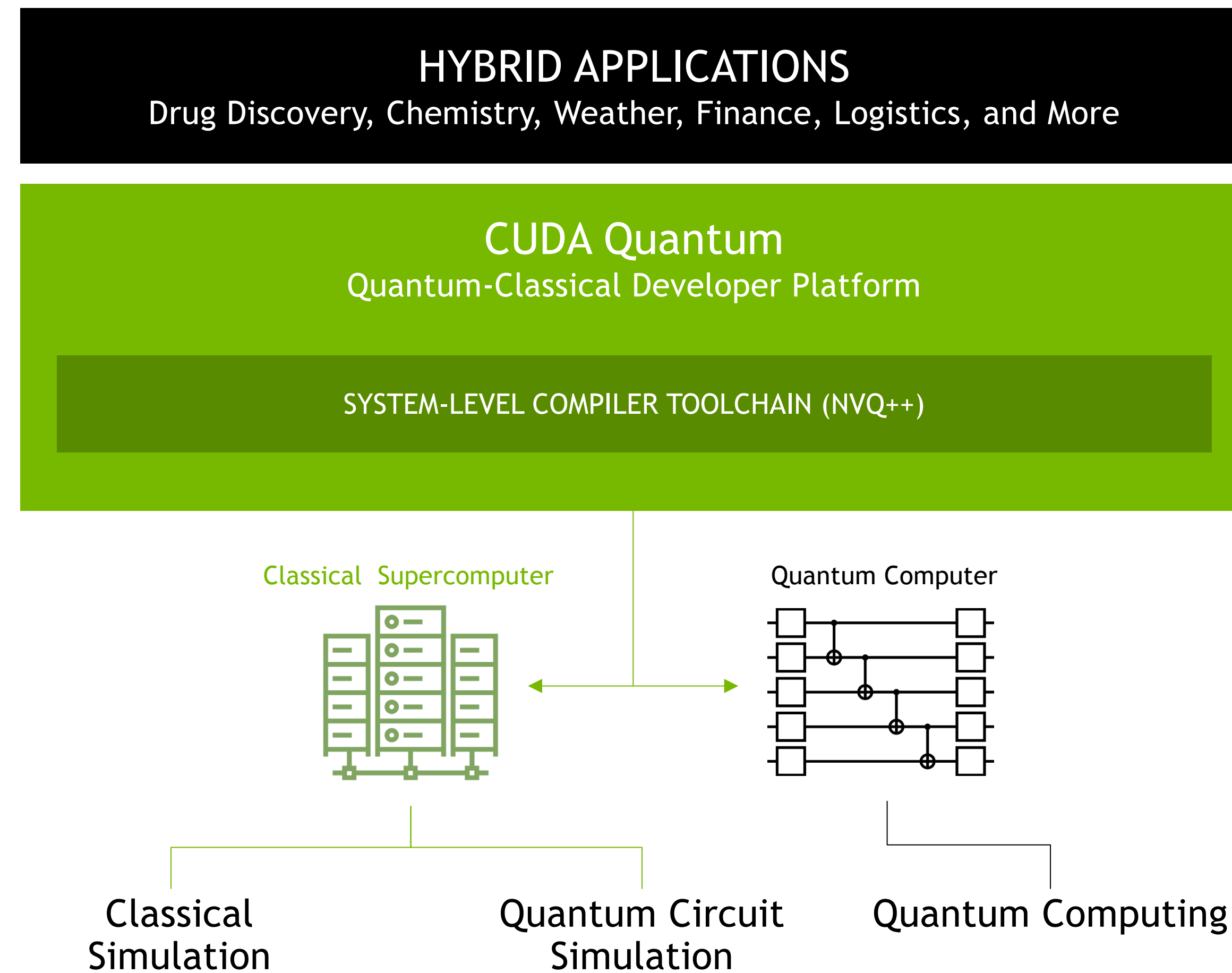
github.com/nvidia/cuda-quantum

<https://catalog.ngc.nvidia.com/orgs/nvidia/containers/cuda-quantum>

CUDA Quantum: Now Available on GitHub and NGC

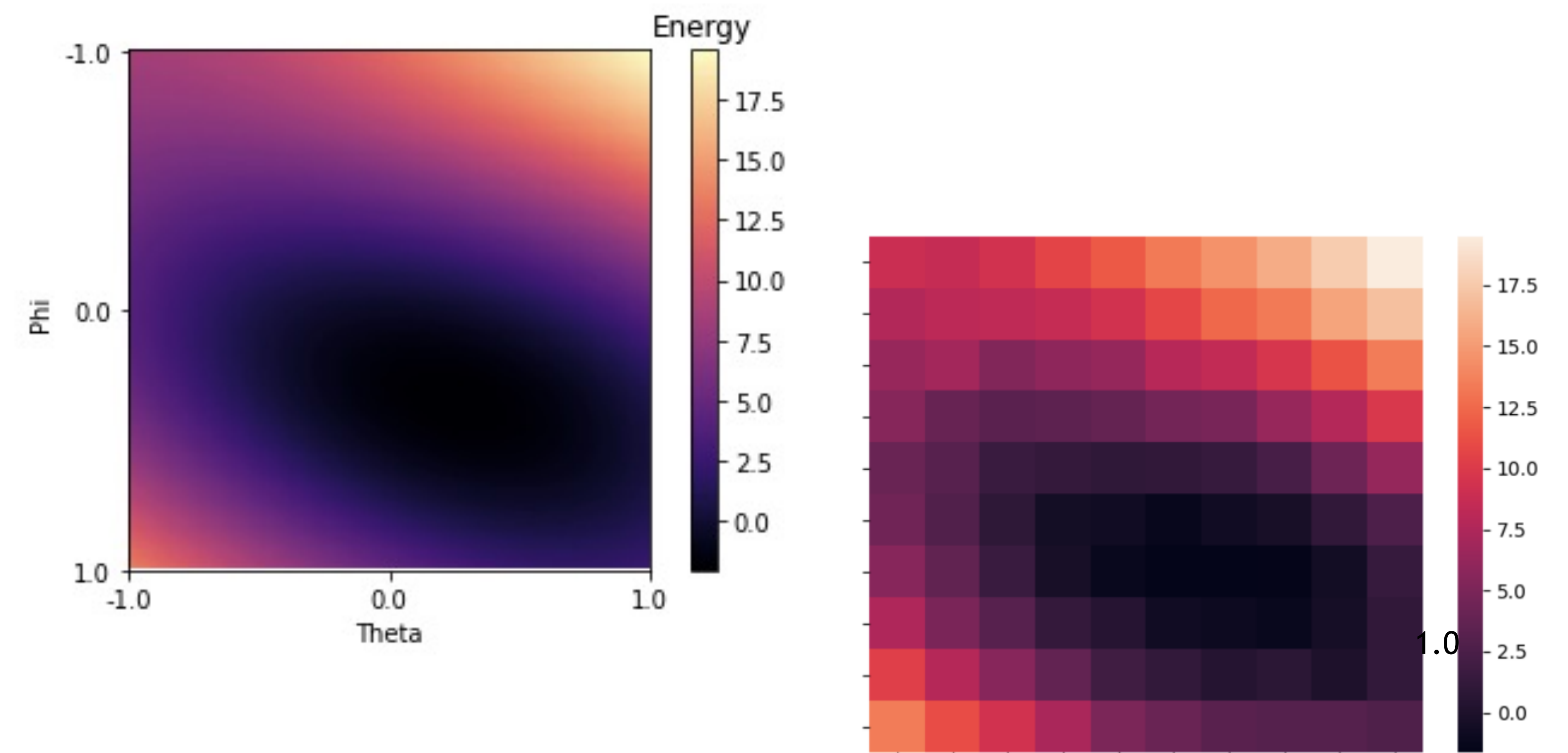
Seamlessly Target any Quantum Resource

CUDA QUANTUM PLATFORM



CUDA Quantum and Quantinuum

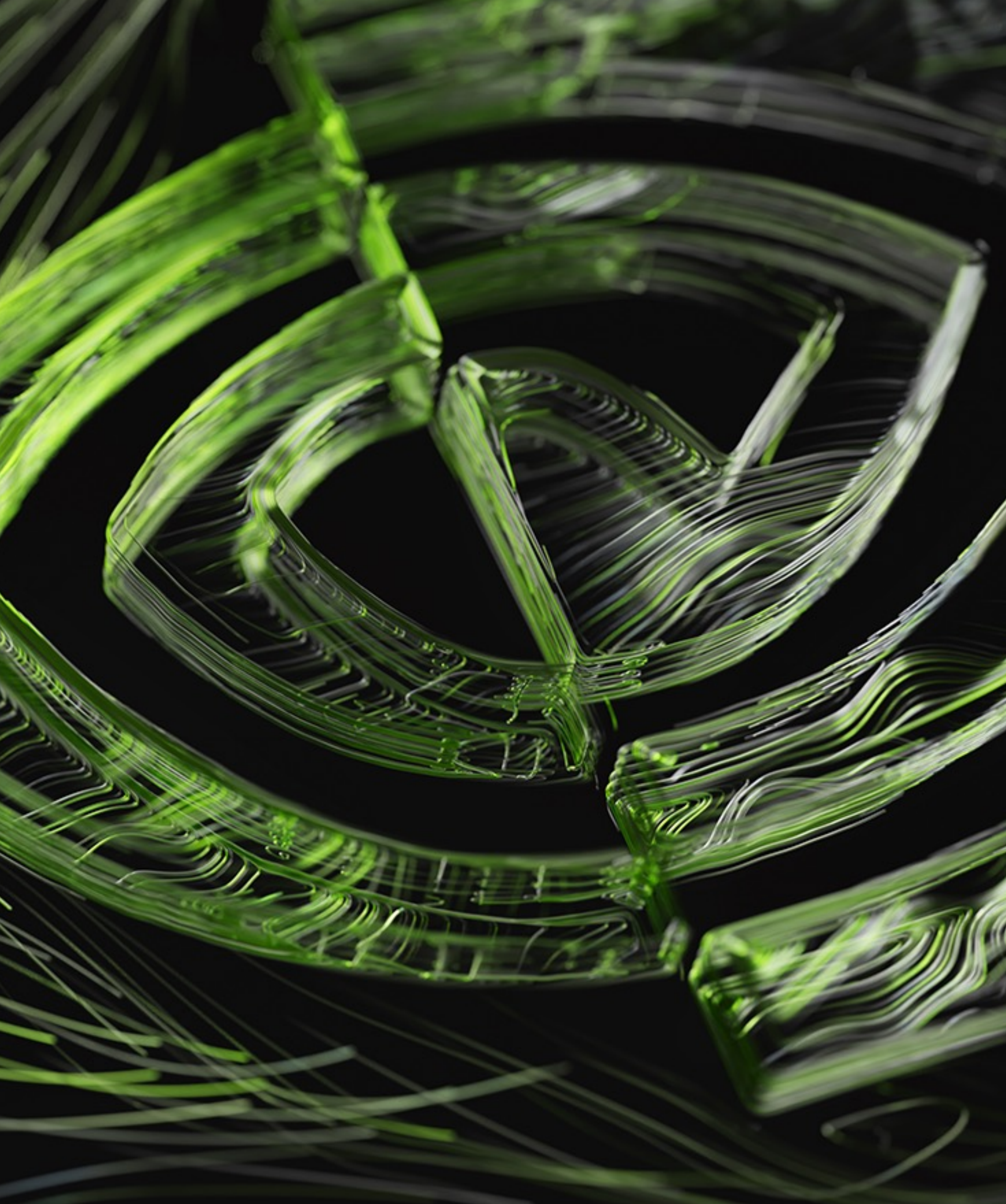
```
nvq++ -qpu=cuquantum vqe.cpp
```



```
nvq++ -qpu=quantinuum:h1 vqe.cpp
```

github.com/nvidia/cuda-quantum

<https://catalog.ngc.nvidia.com/orgs/nvidia/containers/cuda-quantum>



Agenda

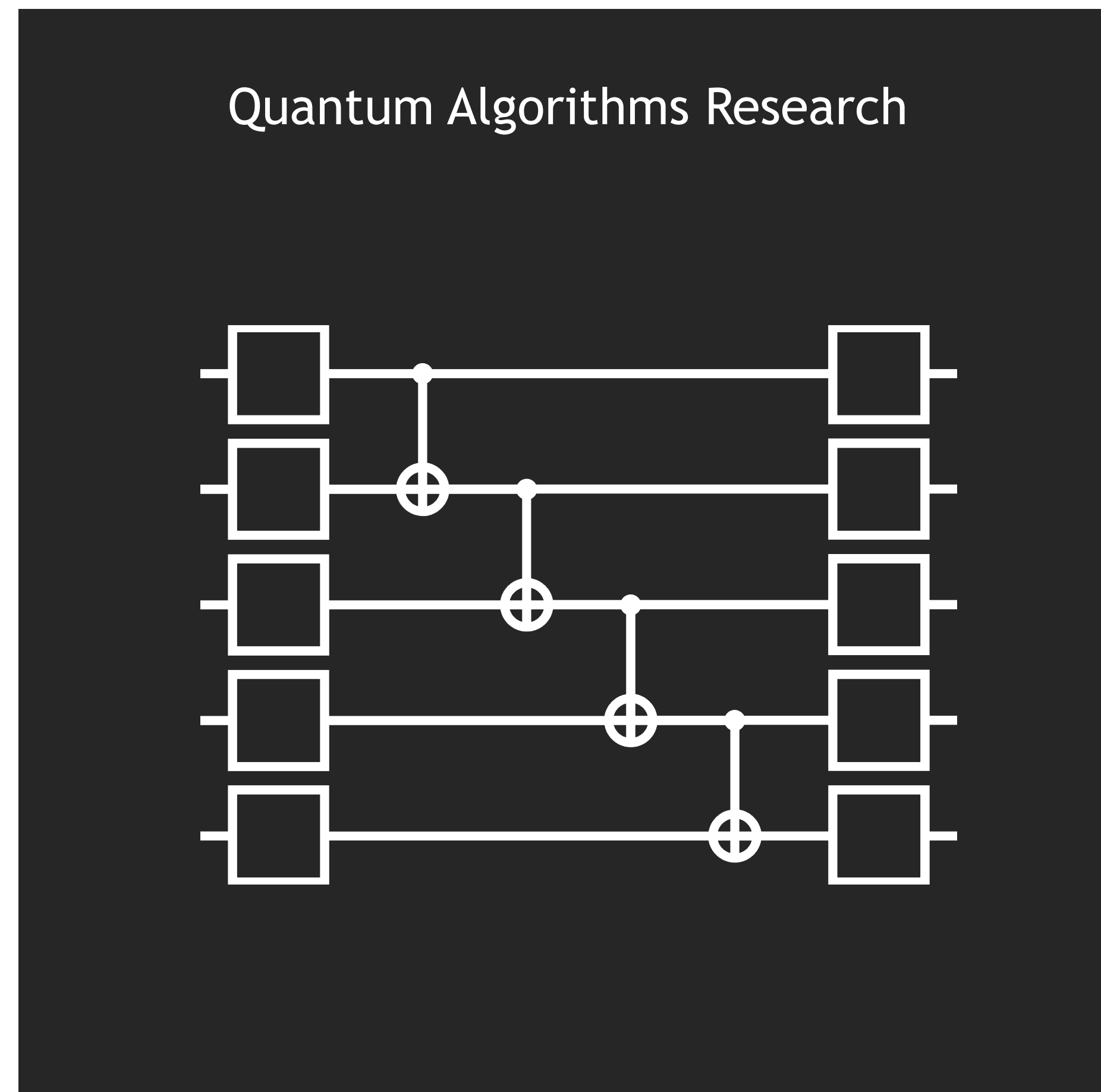
- State of Quantum

- Accelerated Computing for Quantum

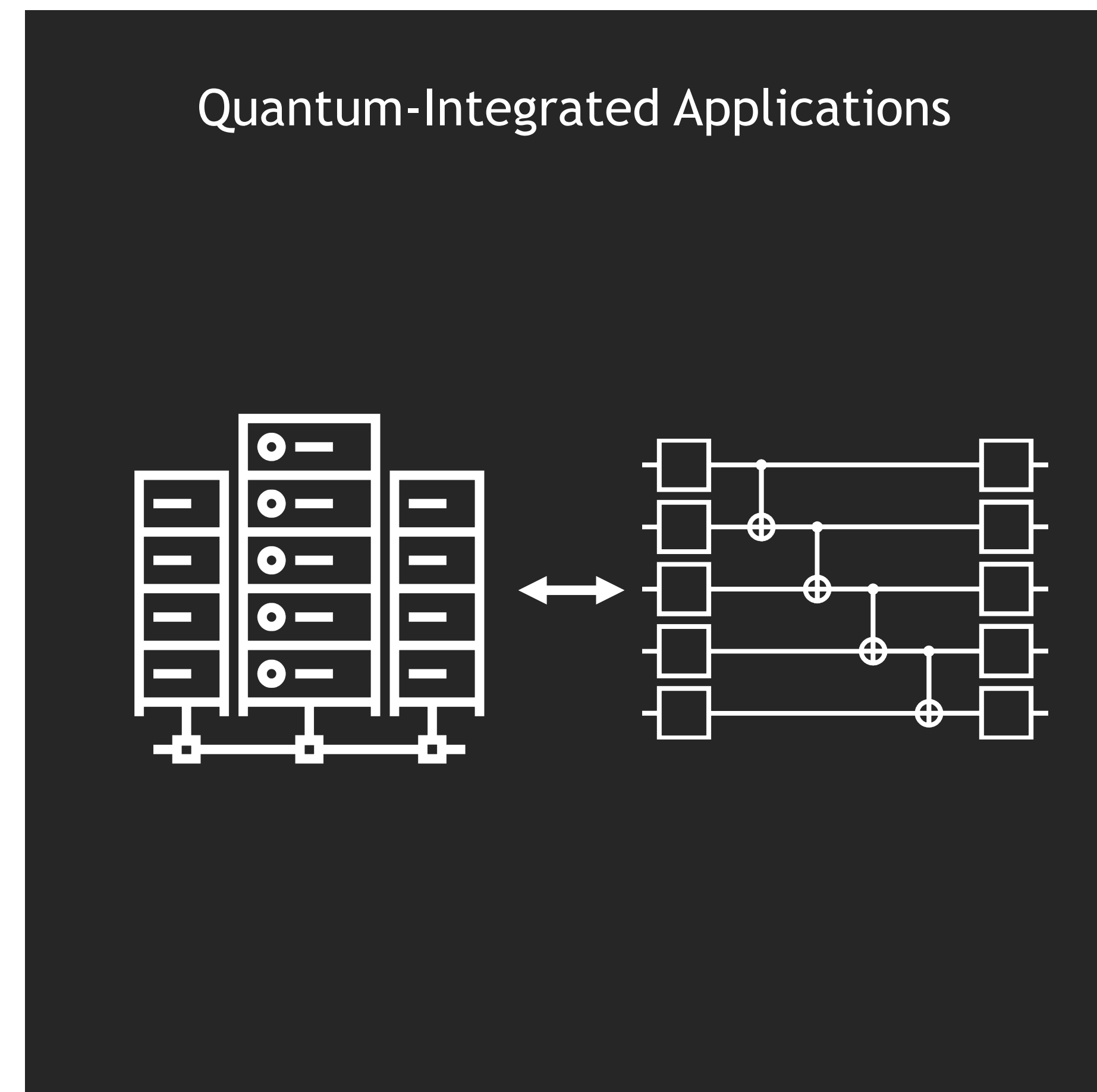
- **Summary**

NVIDIA Quantum

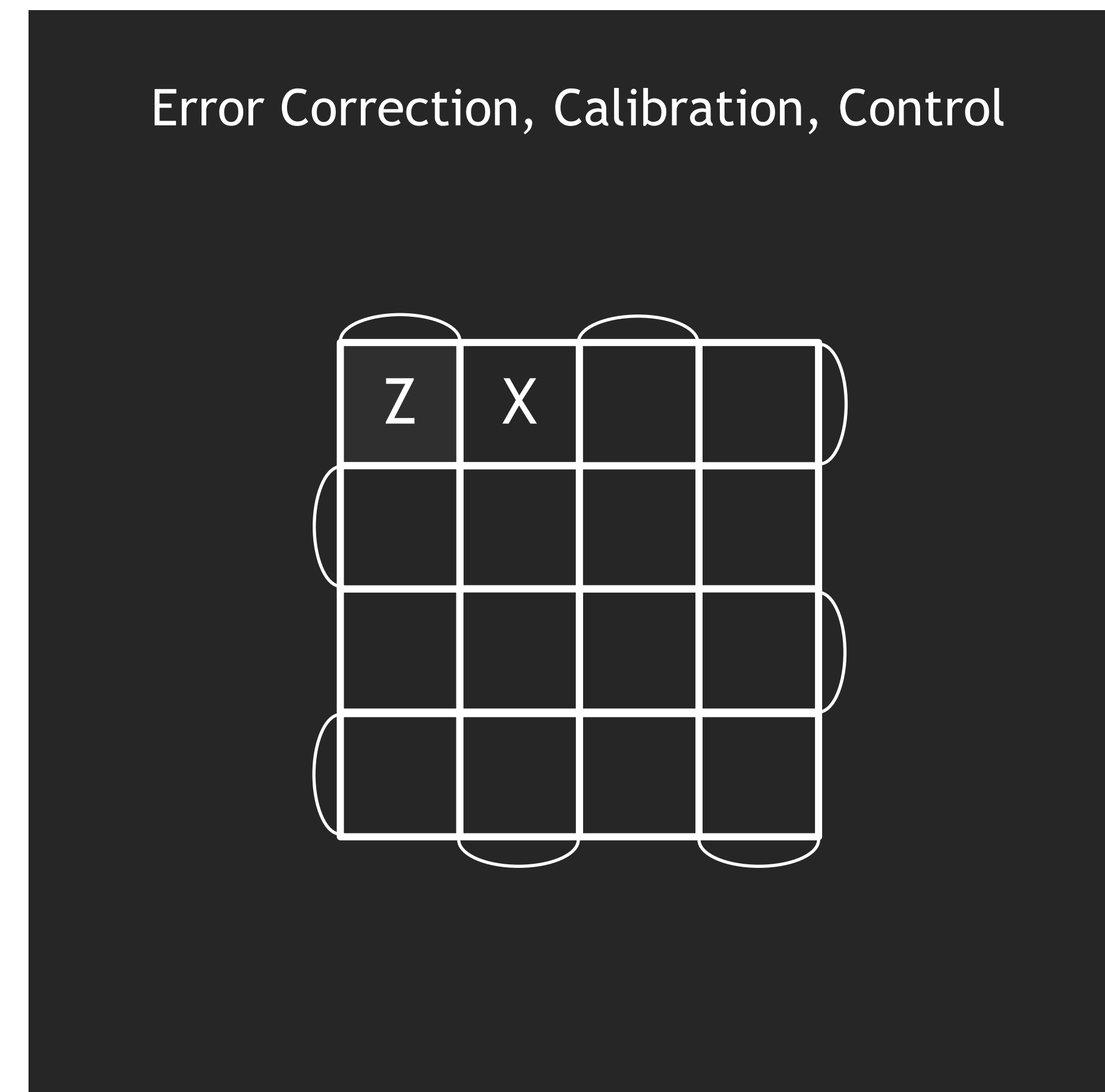
Powering Quantum Simulation and Quantum-Integrated Accelerated Computing



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Accelerated Quantum Simulation



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Quantum Integrated GPU Supercomputing
DGX | HGX | DGX Quantum

